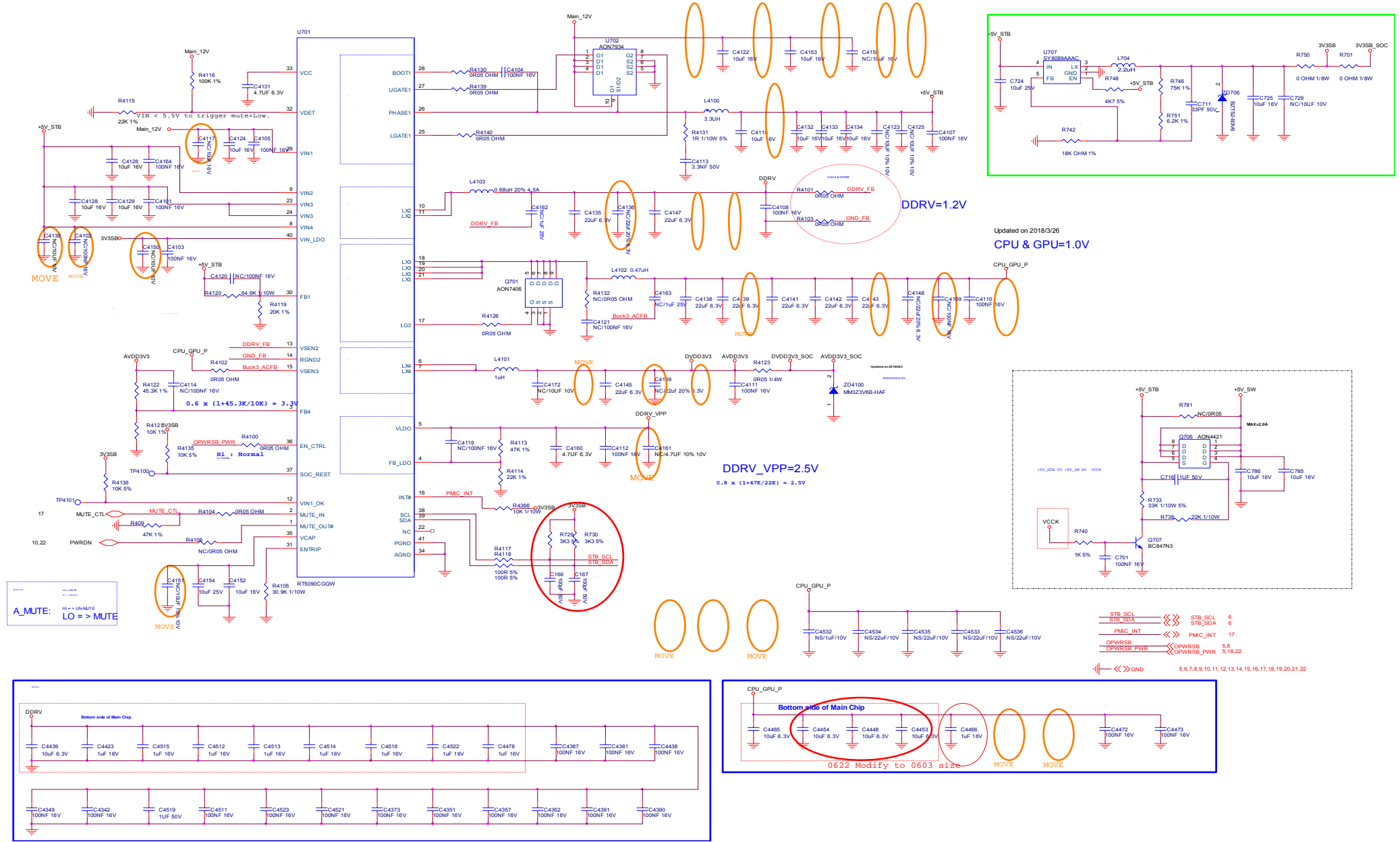
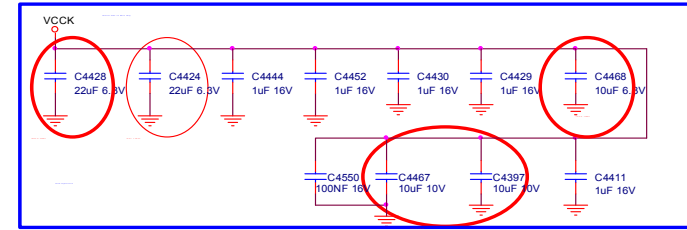
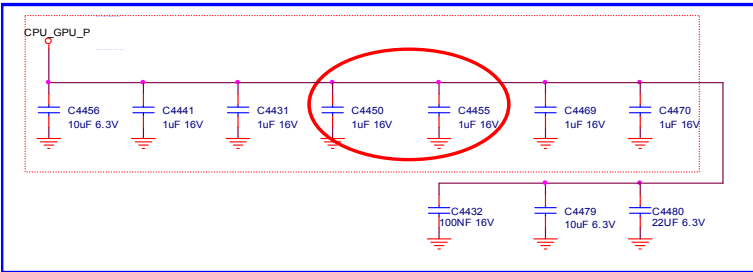
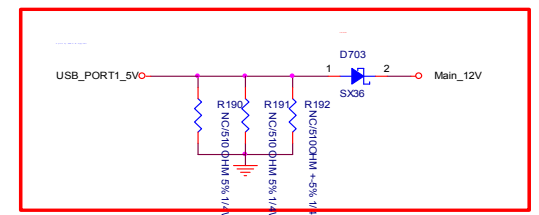
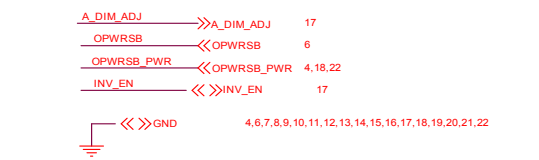
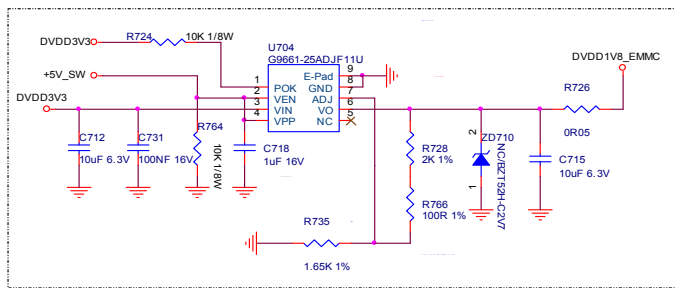
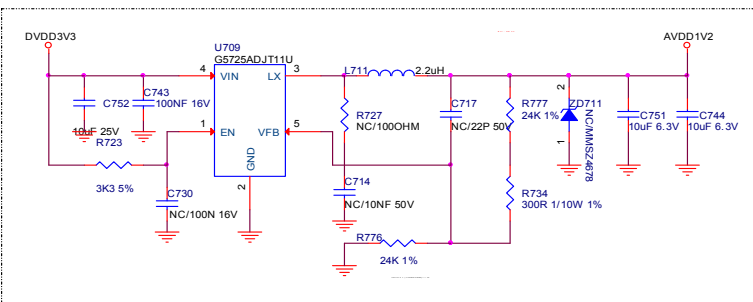
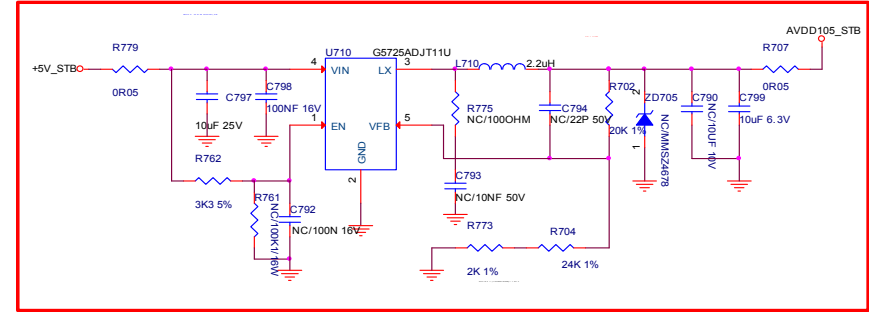
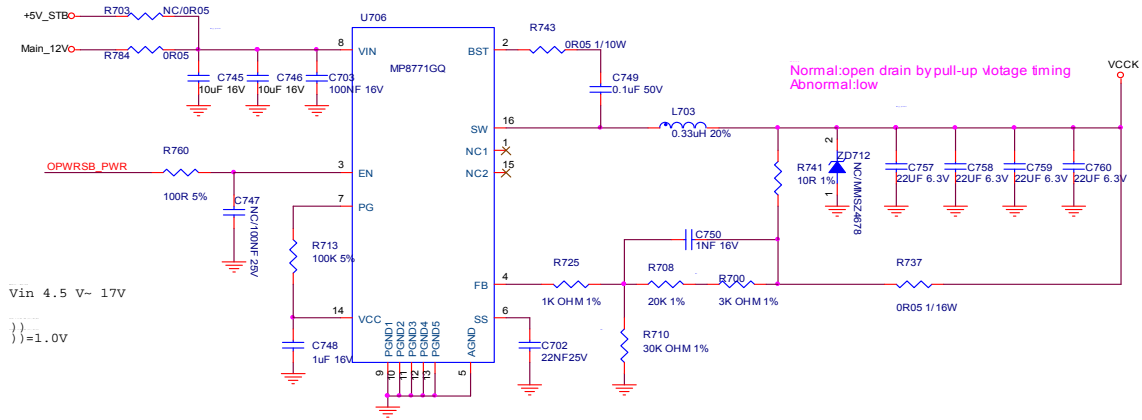
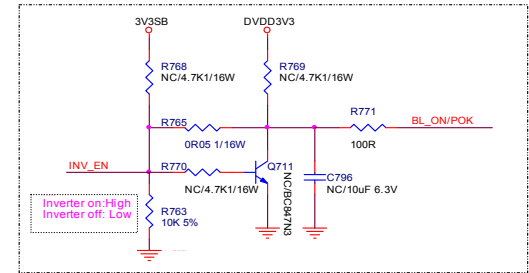
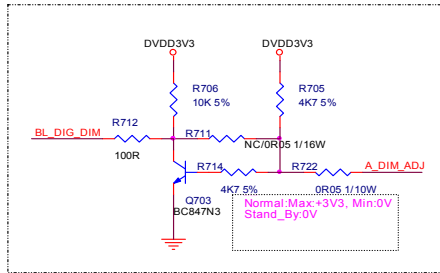
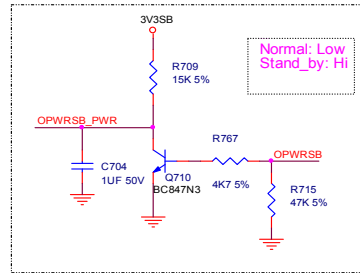
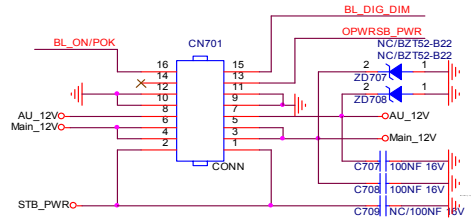


9.3 B 715G9907SSB

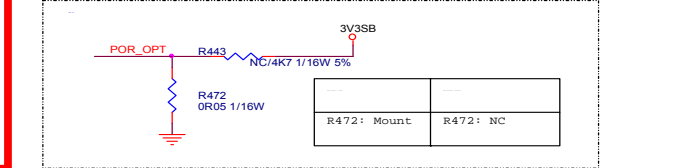
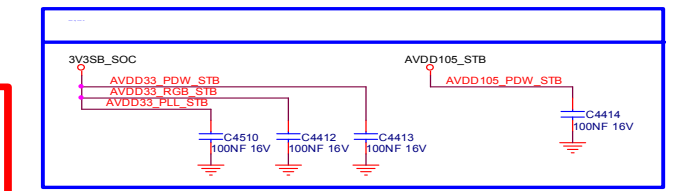
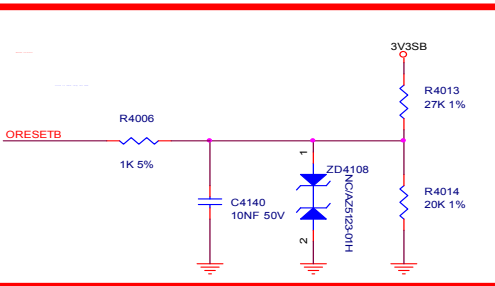
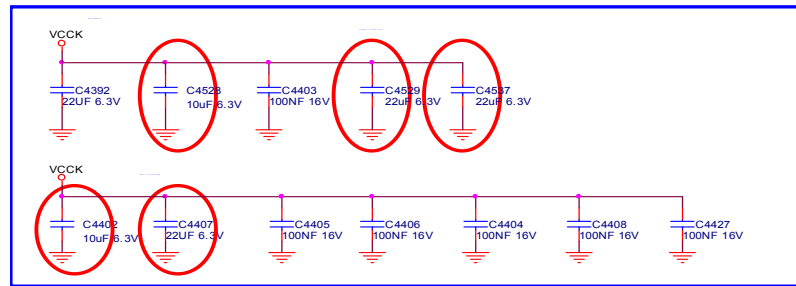
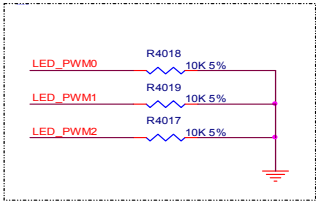
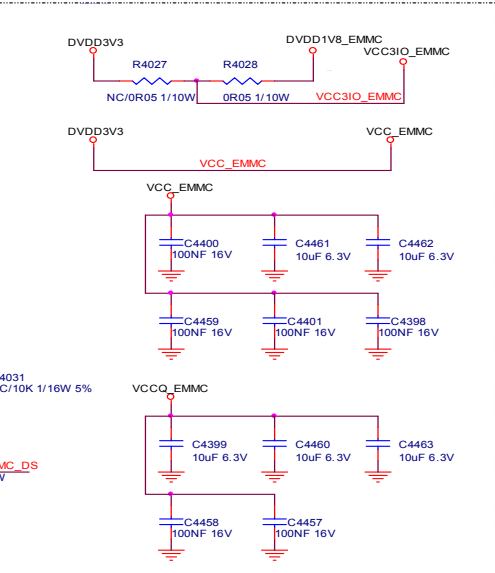
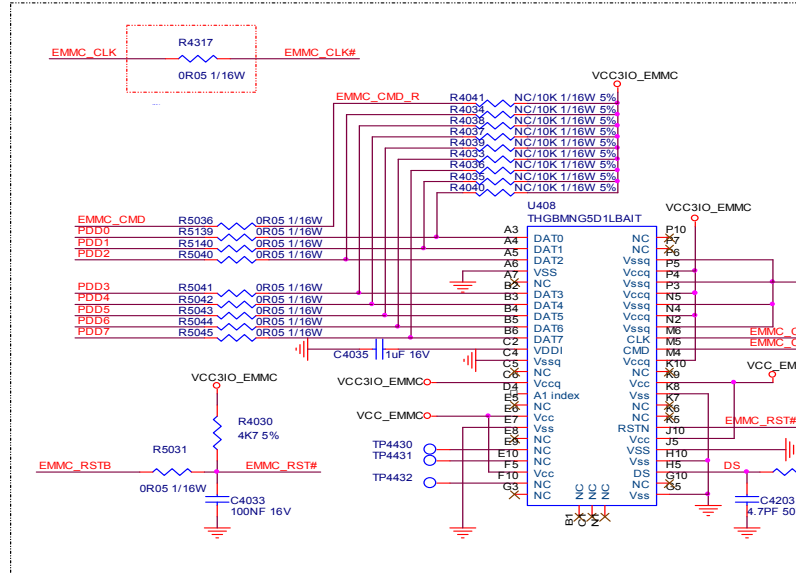
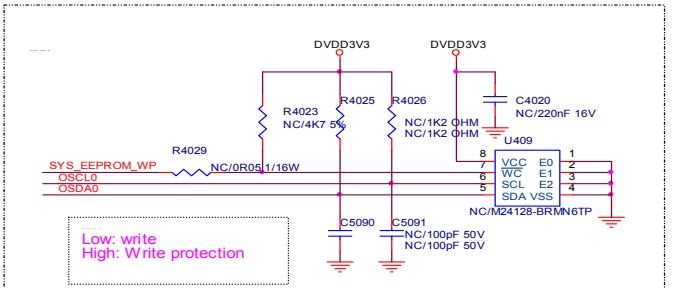
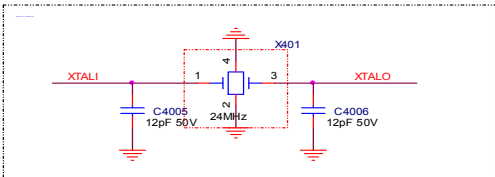
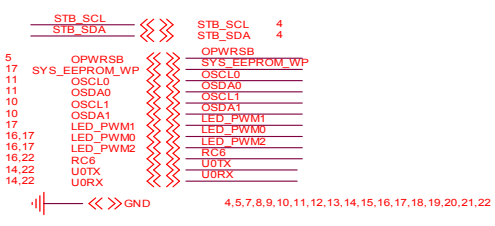
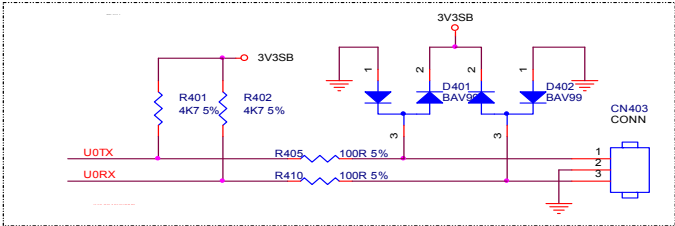
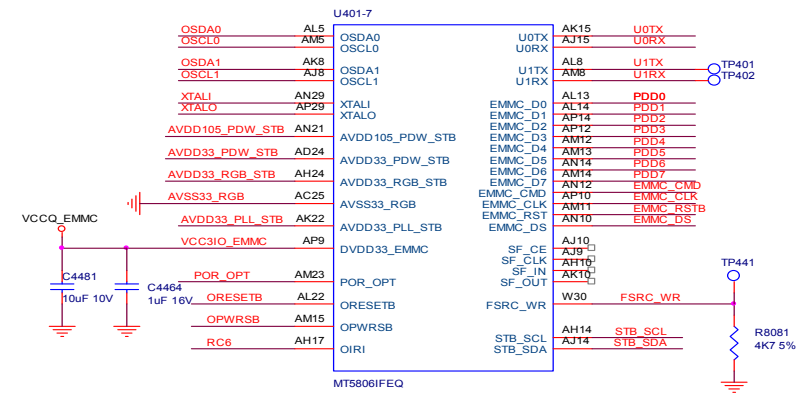
9-3-1POWER



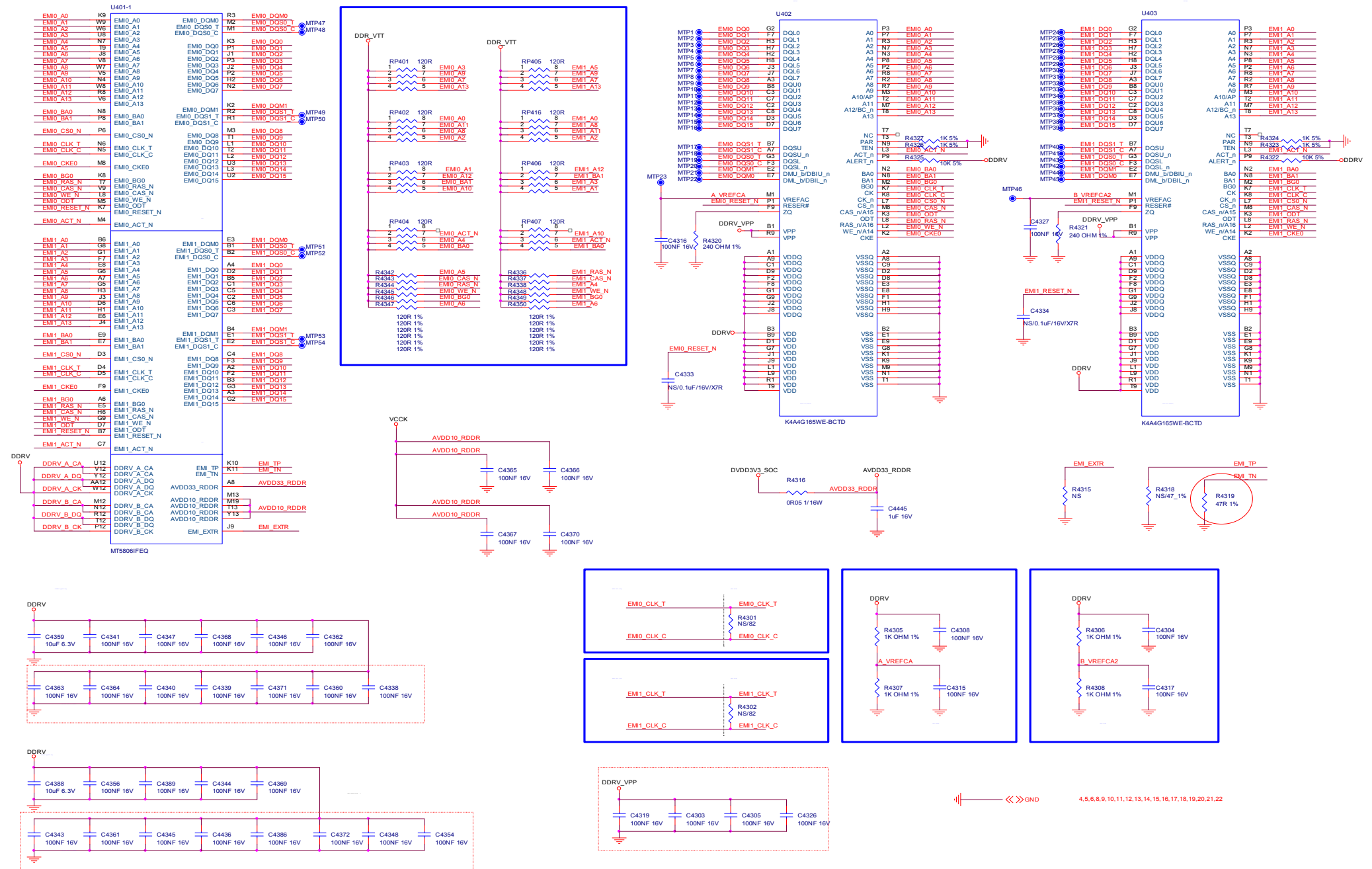
9-3-2 System Power



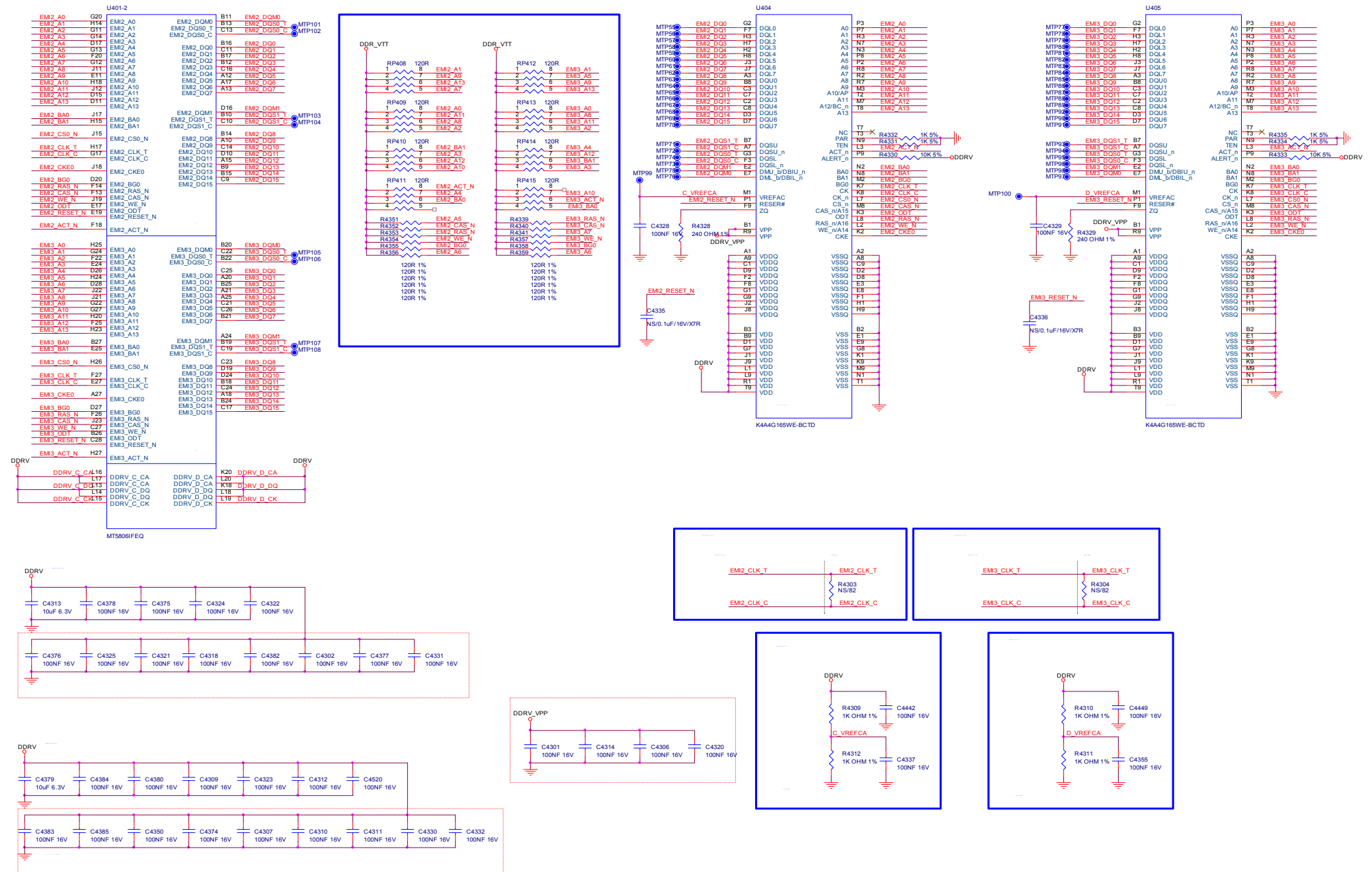
9-3-3 Peripheral



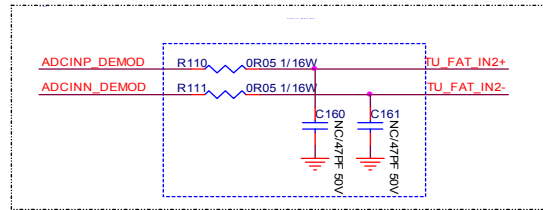
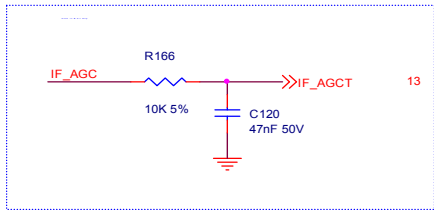
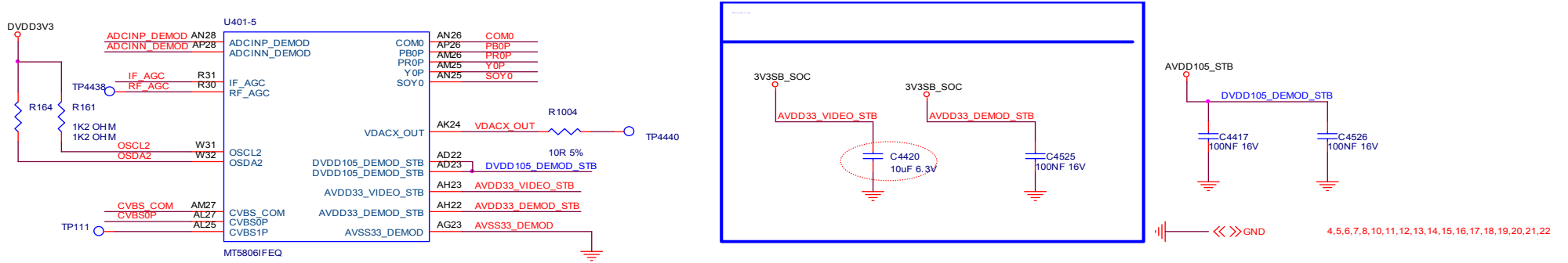
9-3-4 DRR4*2



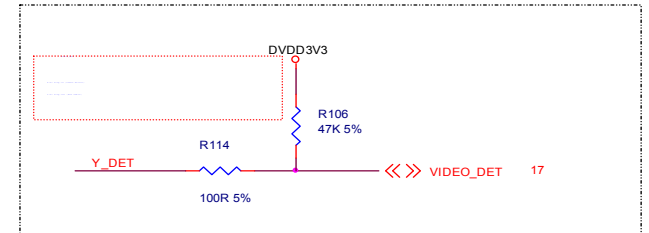
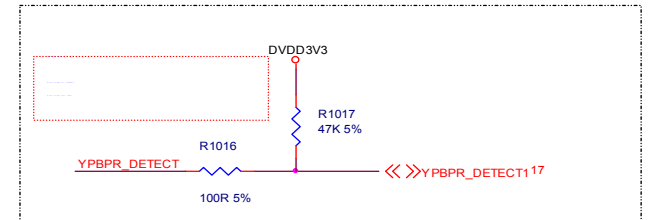
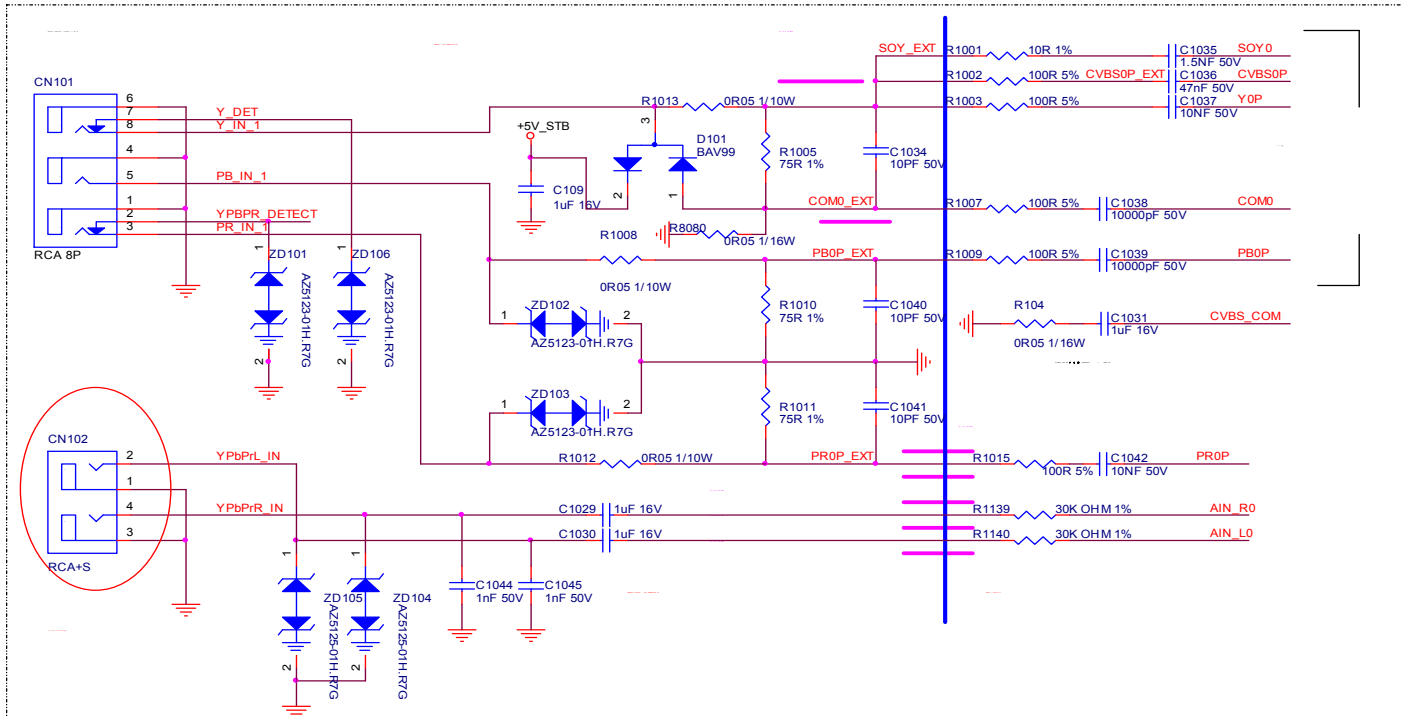
9-3-5 DRR4*2-2



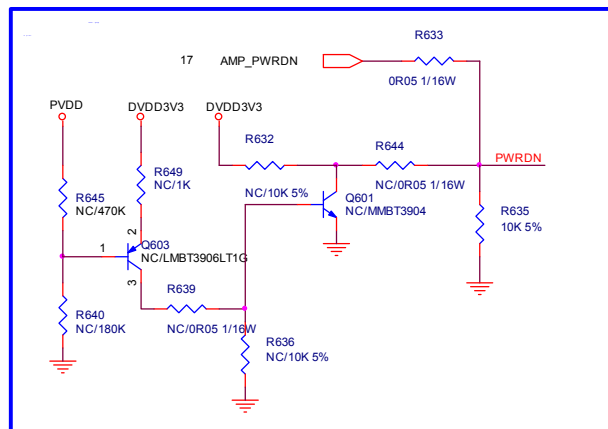
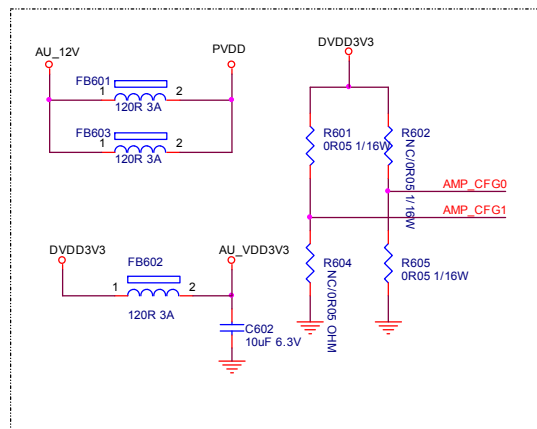
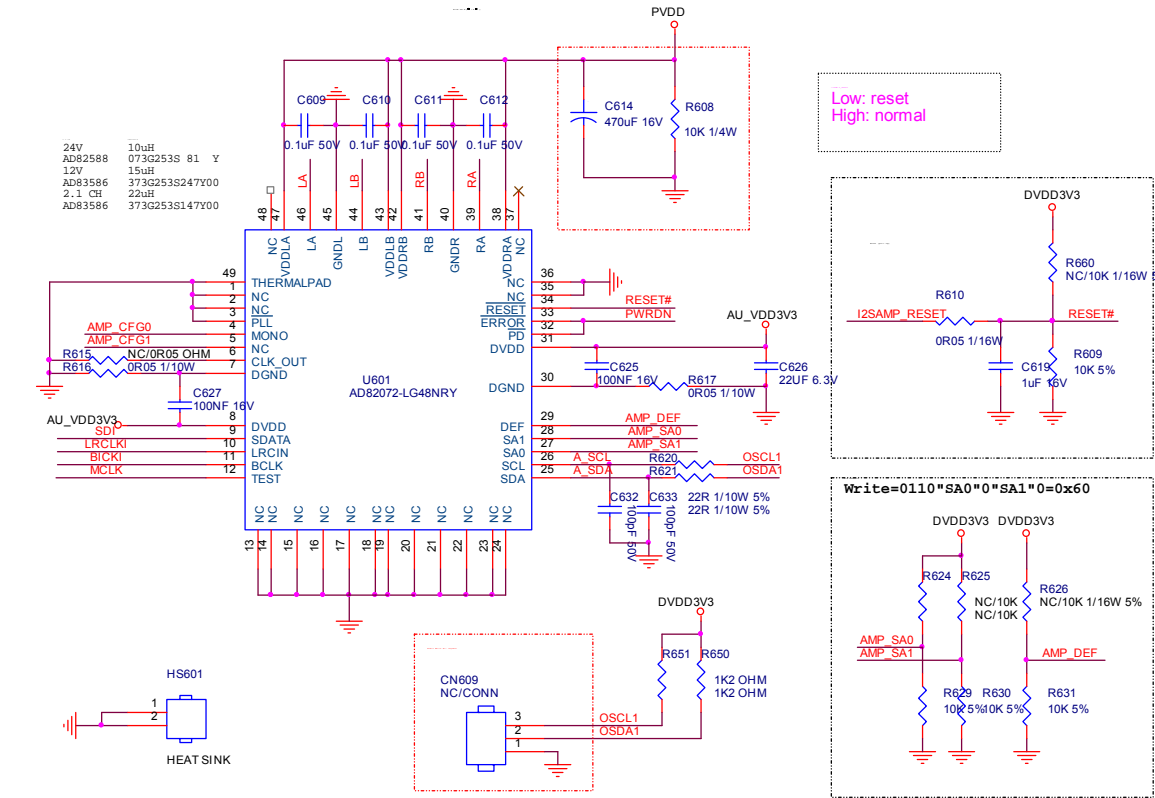
9-3-6 YPbPr/CVBS/SPIDIF



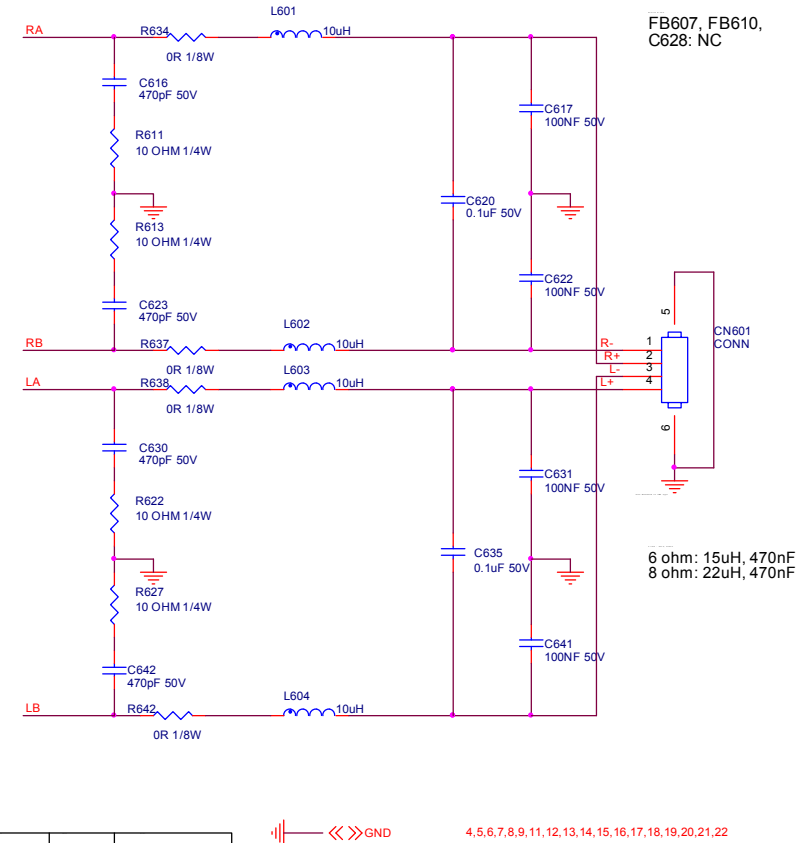
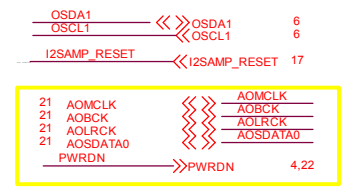
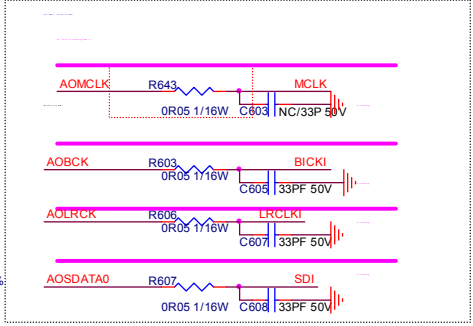
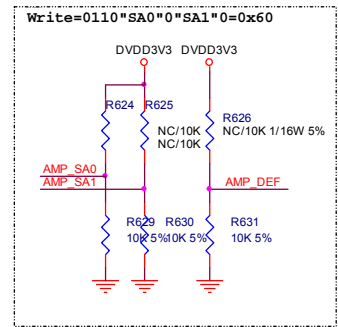
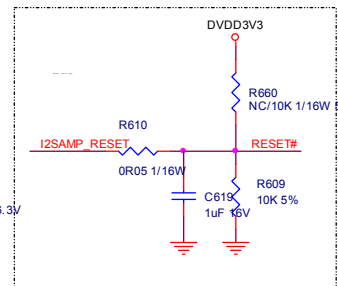
- CVBS0P_EXT \leftrightarrow CVBS0P_EXT 14
- SOY_EXT \leftrightarrow SOY_EXT 14
- COM0_EXT \leftrightarrow COM0_EXT 14
- PB0P_EXT \leftrightarrow PB0P_EXT 14
- PR0P_EXT \leftrightarrow PR0P_EXT 14
- AL10 \leftrightarrow AL10 21,22
- AR10 \leftrightarrow AR10 21,22
- OSCL2 \leftrightarrow OSCL2 13
- OSDA2 \leftrightarrow OSDA2 13
- DISEQC_OUT \leftrightarrow DISEQC_OUT 13,14
- AIN_R0 \leftrightarrow AIN_R0 14,21
- AIN_L0 \leftrightarrow AIN_L0 14,21
- TU_FAT_IN2+ \leftrightarrow TU_FAT_IN2+13
- TU_FAT_IN2- \leftrightarrow TU_FAT_IN2-13



9-3-7 AUDIO AMP

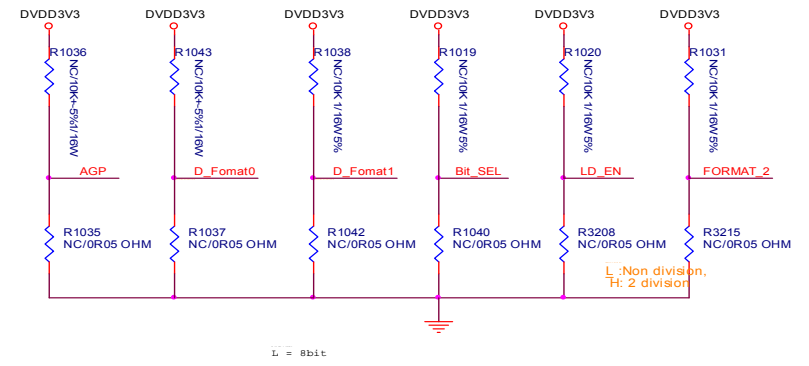
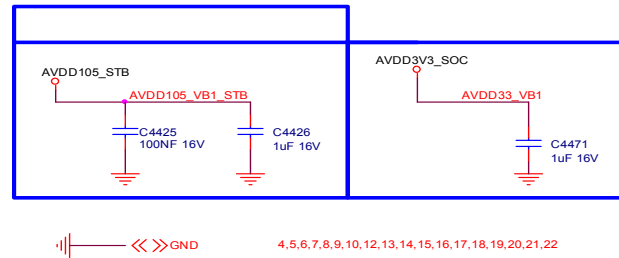
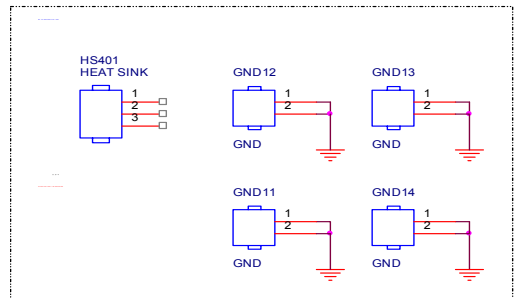
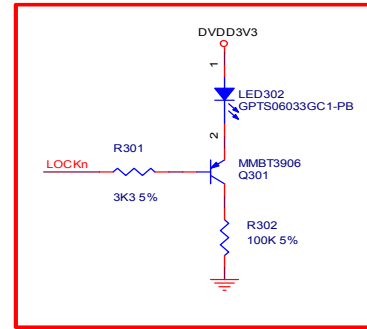
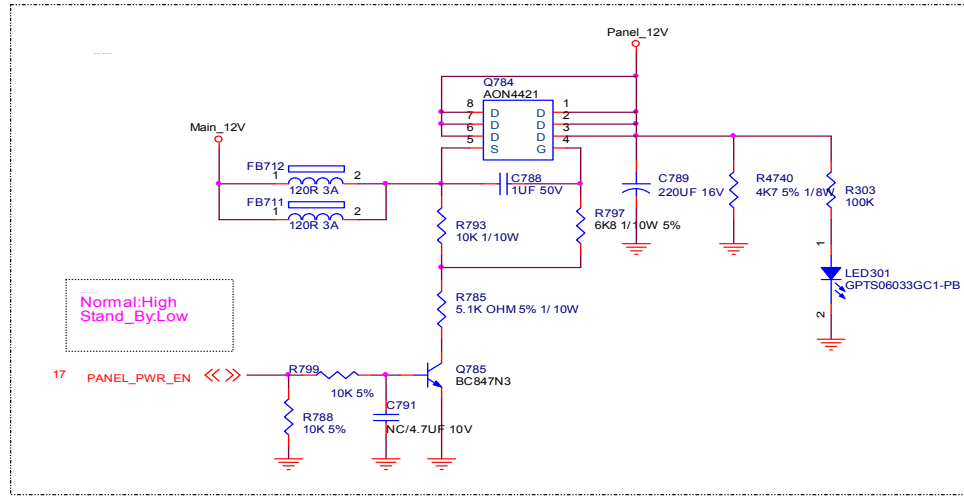
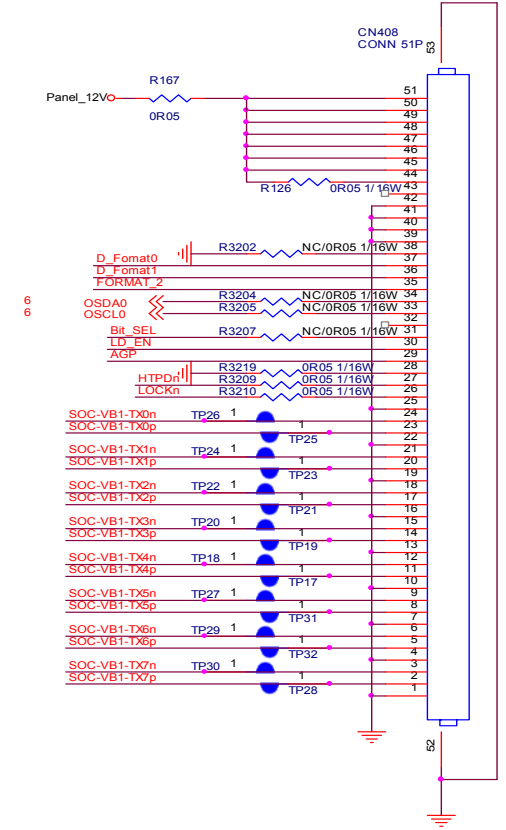
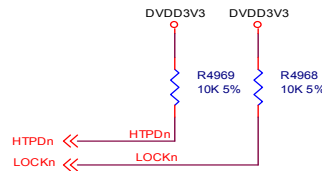
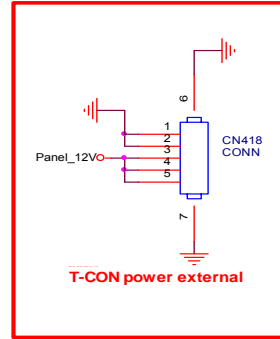
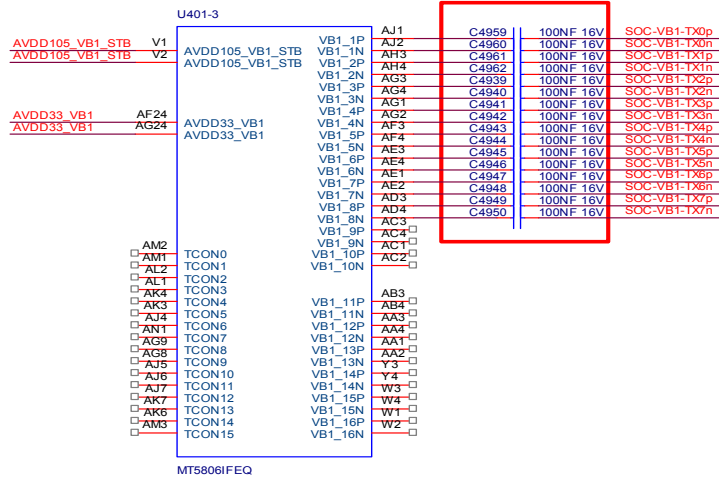


Low: reset
High: normal

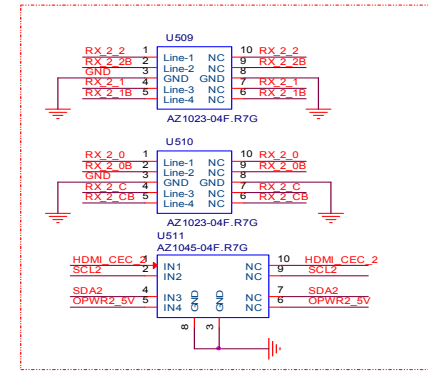
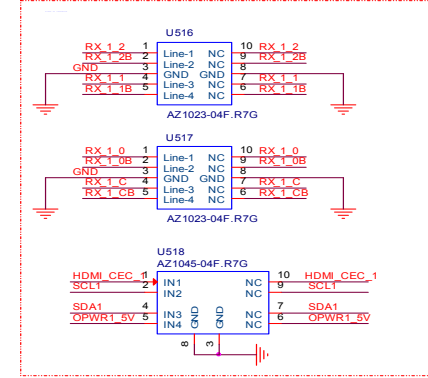
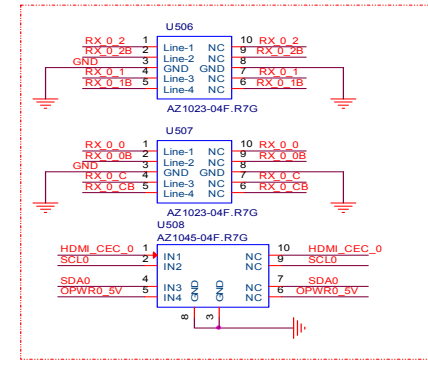
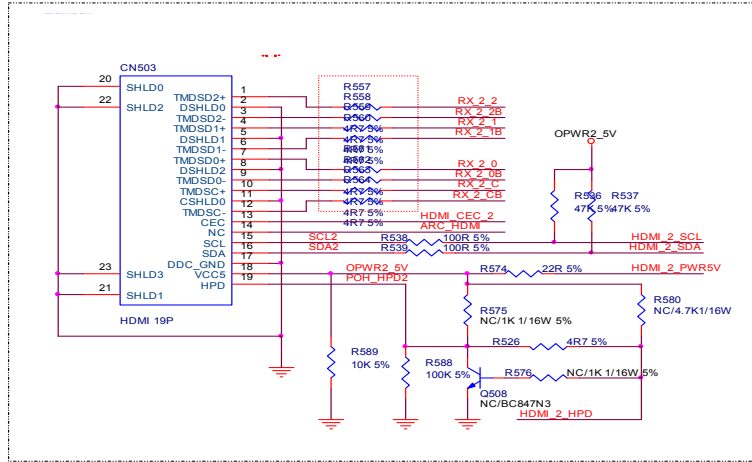
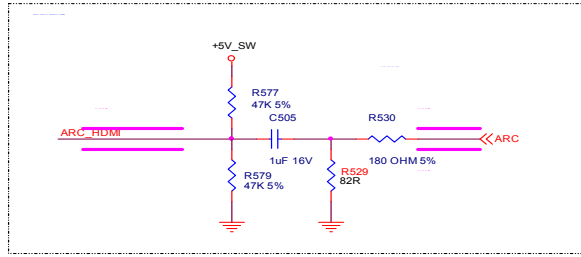
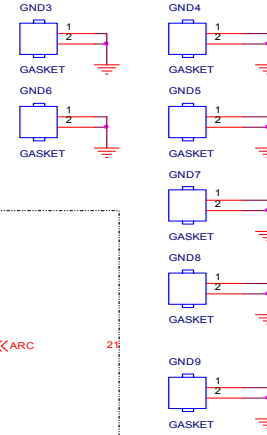
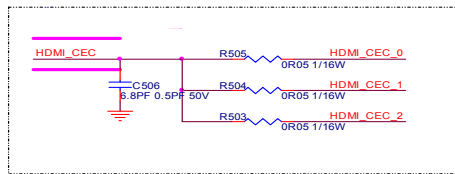
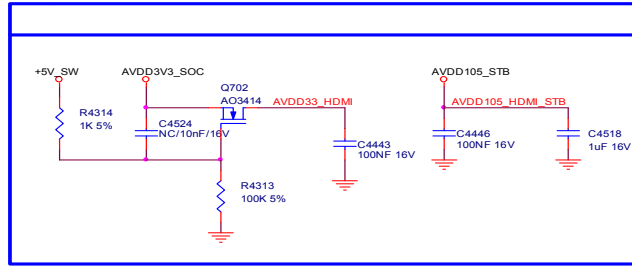
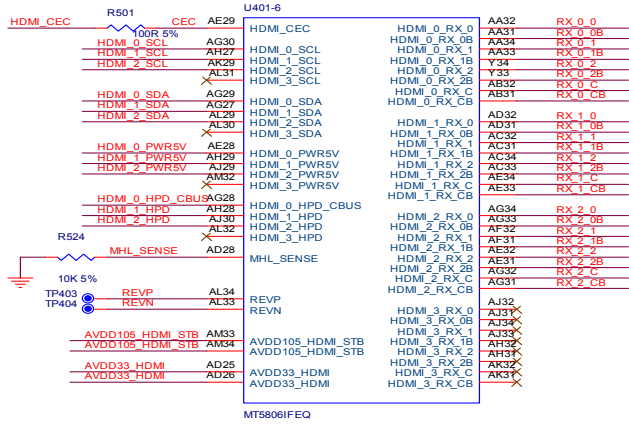


<<< GND 4,5,6,7,8,9,11,12,13,14,15,16,17,18,19,20,21,22

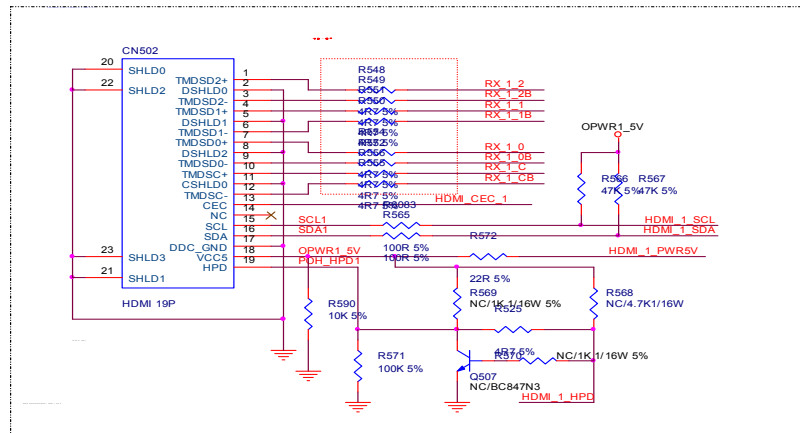
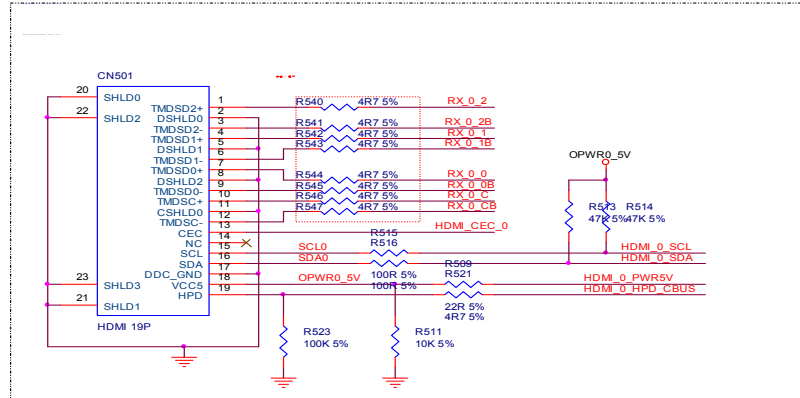
9-3-8 VB1 output



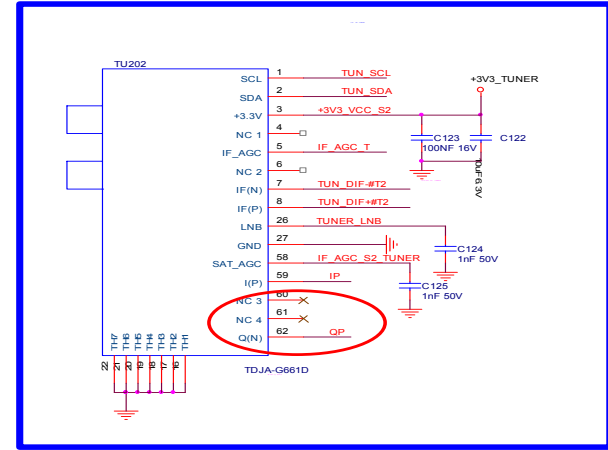
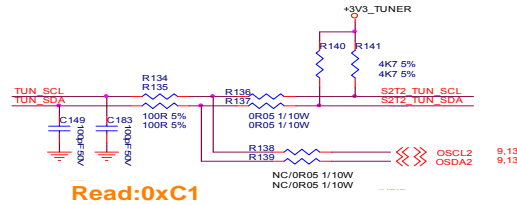
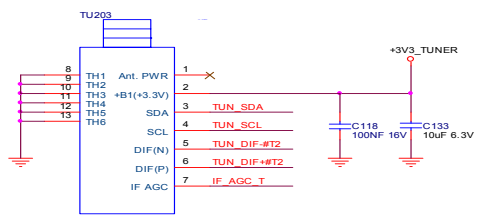
9-3-9 HDMI*



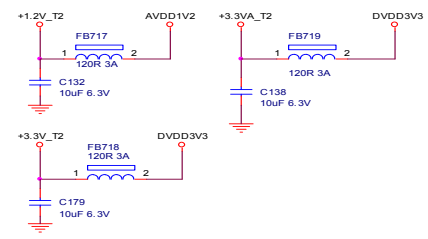
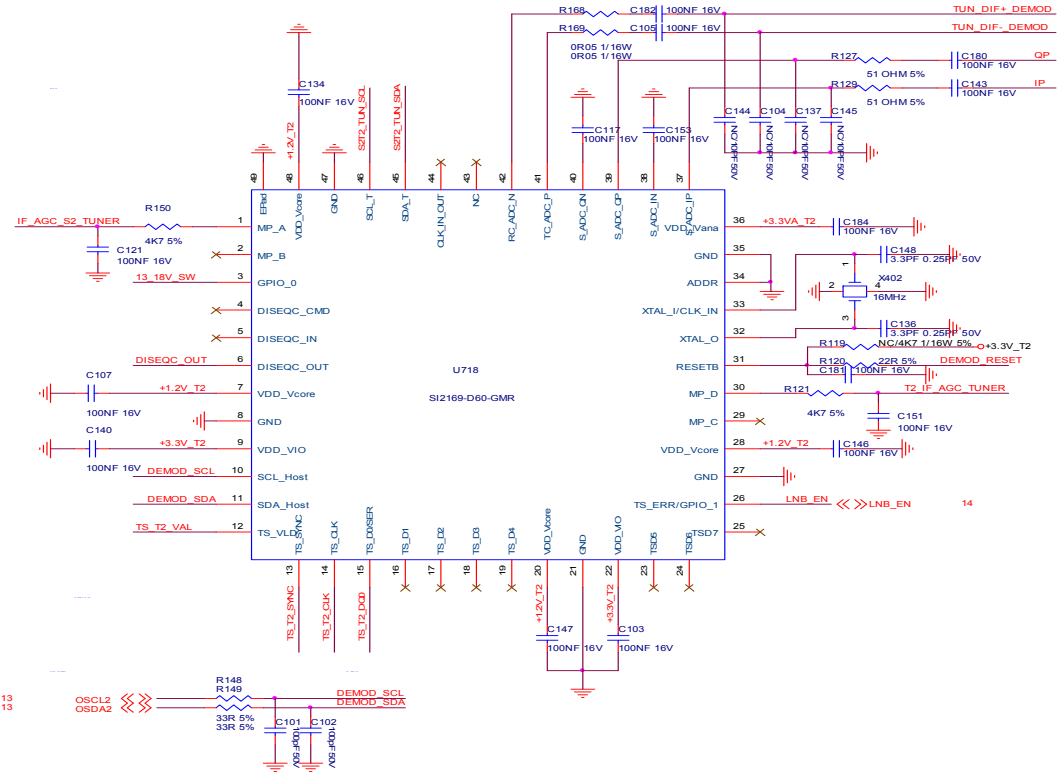
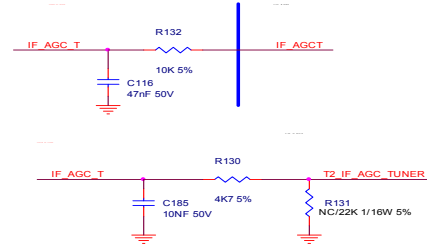
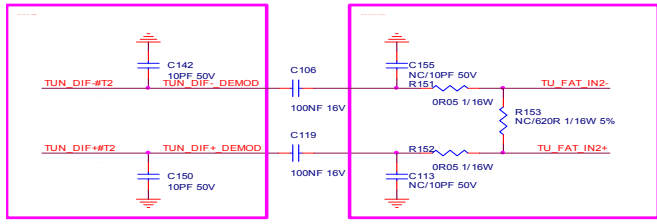
4,5,6,7,8,9,10,11,13,14,15,16,17,18,19,20,21,22



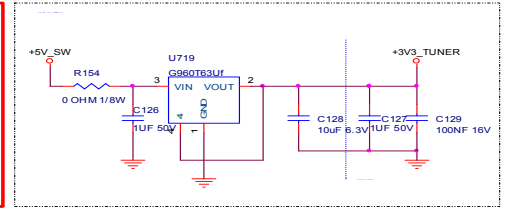
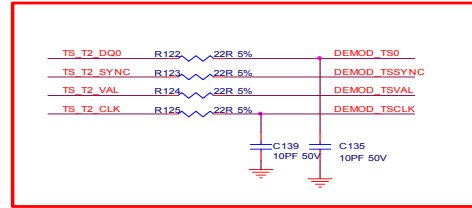
9-3-10 Tuner & Demod*



Read:0xC1

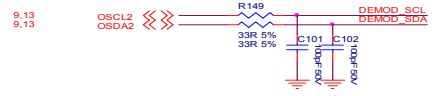


- DEMOM_TSSYNC <<>> DEMOM_TSSYNC7
- DEMOM_TSVAL <<>> DEMOM_TSVAL 17
- DEMOM_TSLK <<>> DEMOM_TSLK7
- DEMOM_TSD0 <<>> DEMOM_TSD0 17
- IF_AGC_T <<>> IF_AGC_T 9
- TU_FAT_IN2+ <<>> TU_FAT_IN2+ 9
- TU_FAT_IN2- <<>> TU_FAT_IN2- 9
- 13_18V_SW <<>> 13_18V_SW 14
- TUNER_LNB <<>> TUNER_LNB 14
- DISEQC_OUT <<>> DISEQC_OUT 14
- DEMOM_RESET <<>> DEMOM_RESET 17

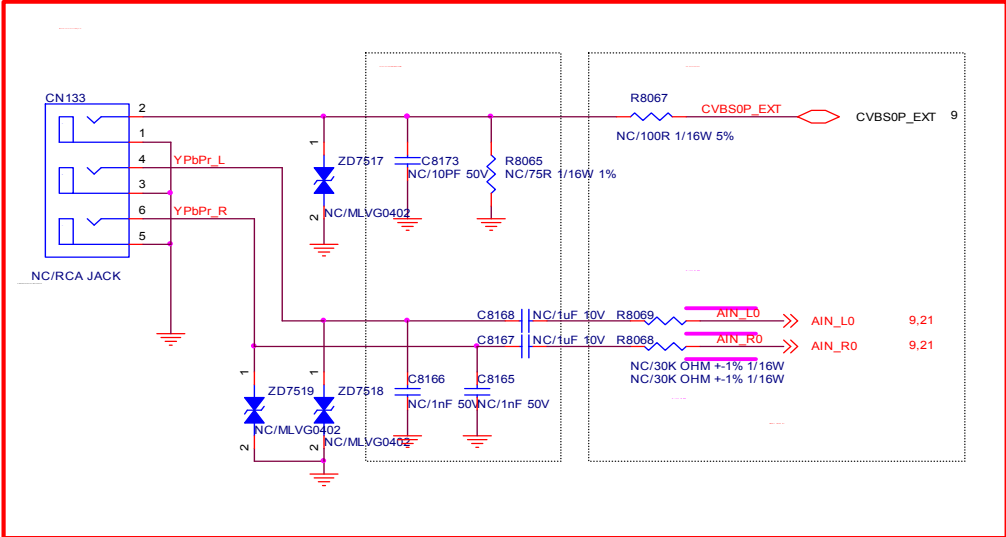
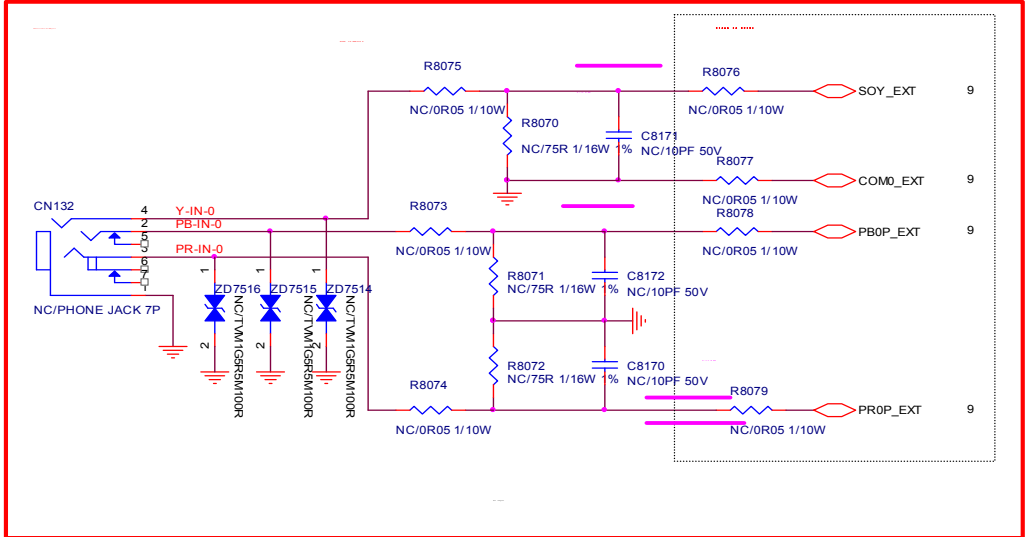
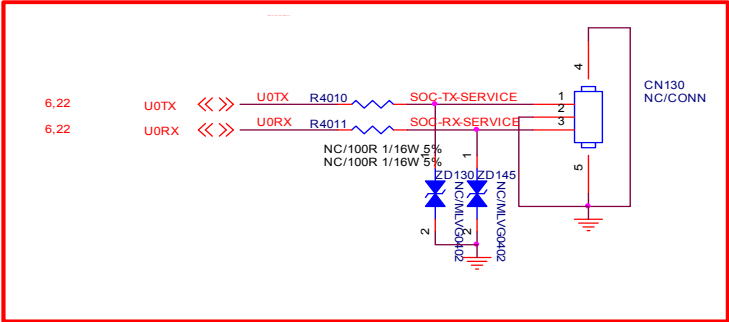
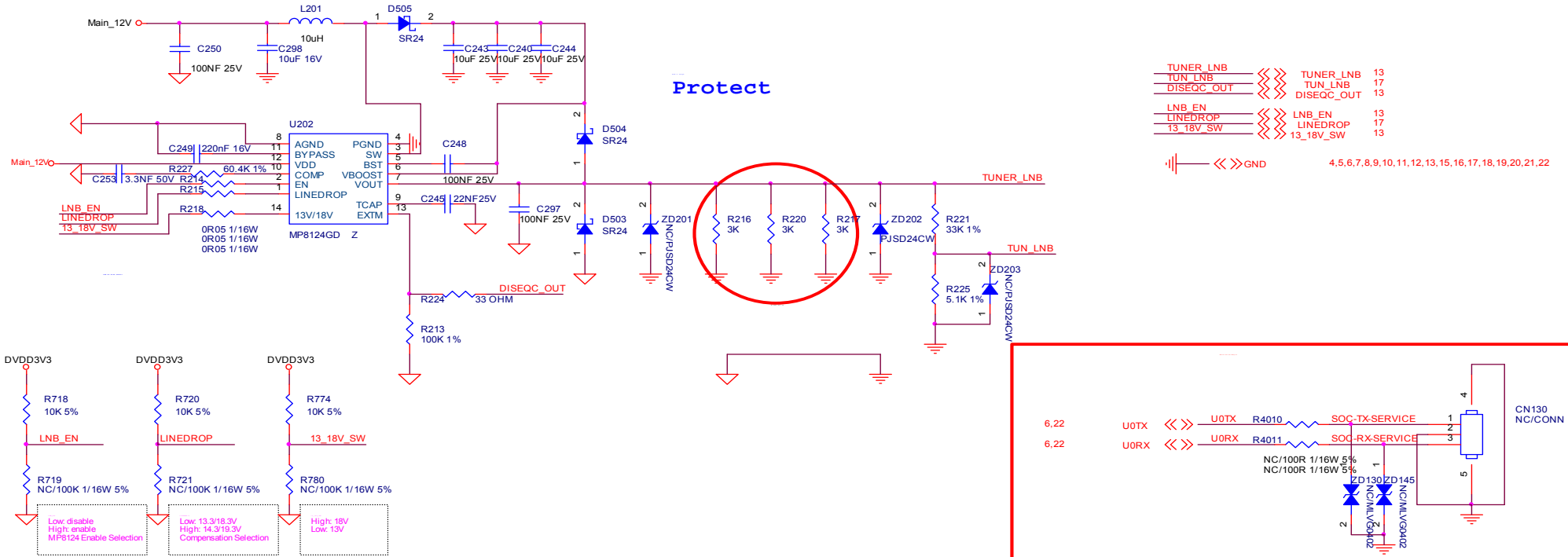


0575048610
356G0575048613

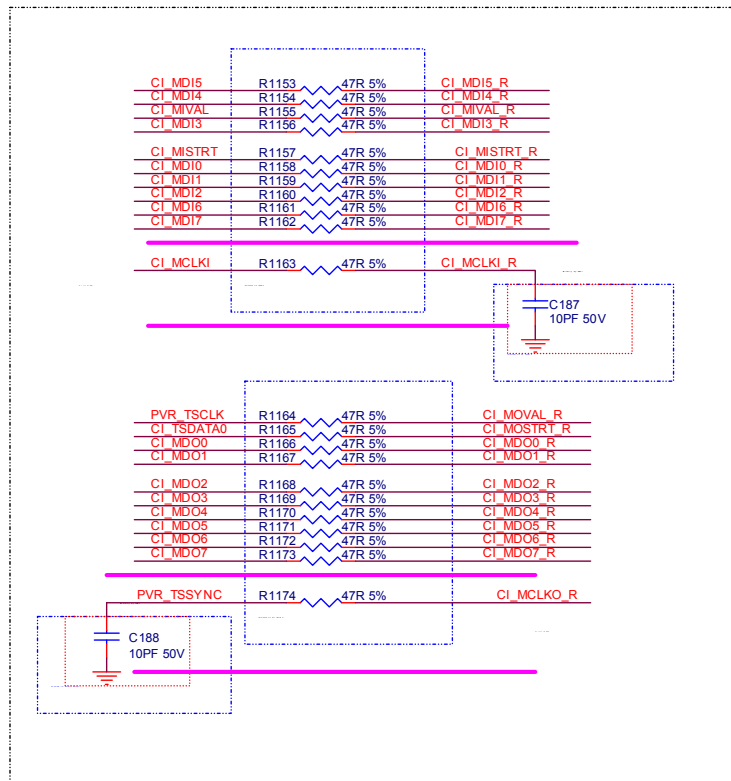
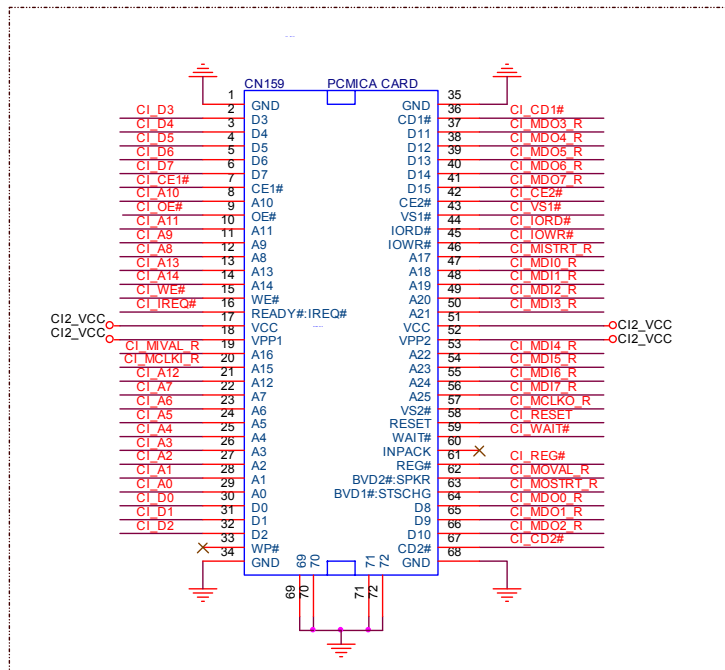
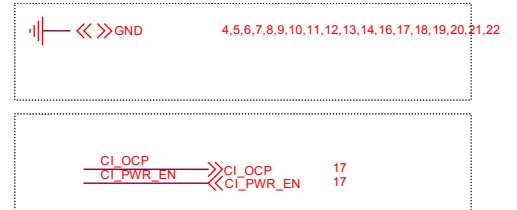
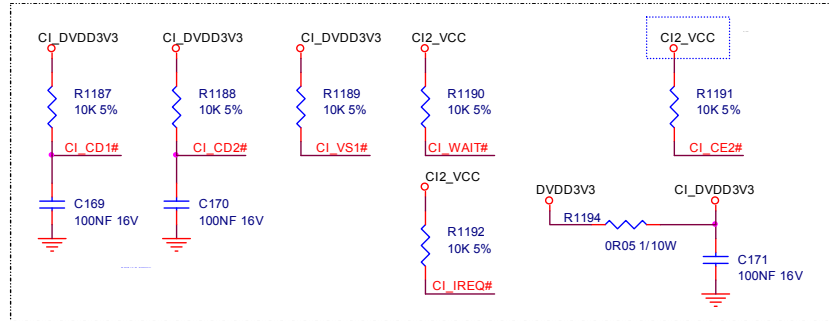
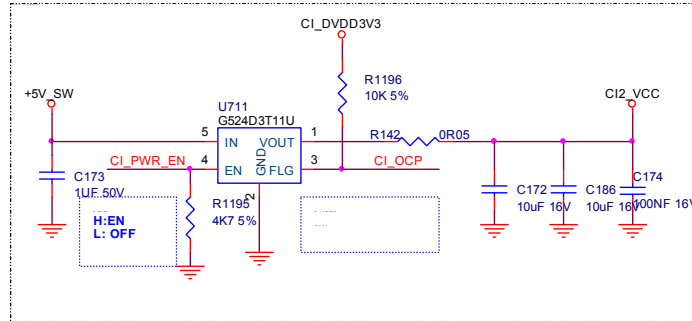
<<>> GND 4,5,6,7,8,9,10,11,12,14,15,16,17,18,19,20,21,22



9-3-11 LNB & EXT AV INPUT



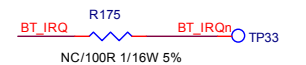
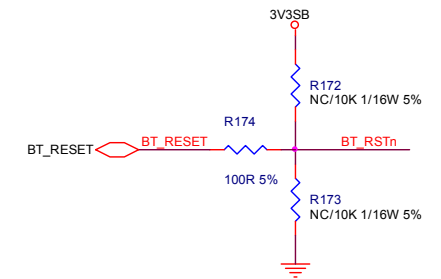
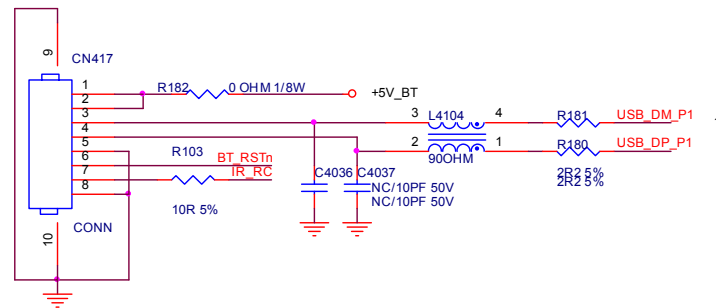
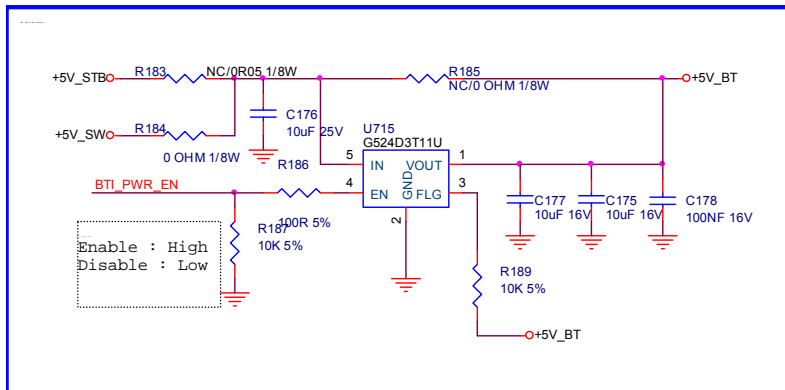
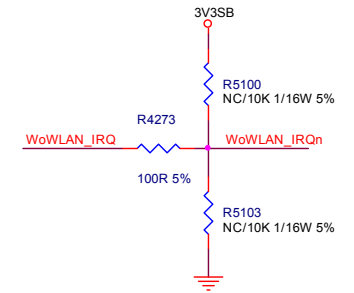
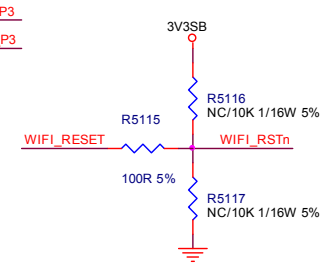
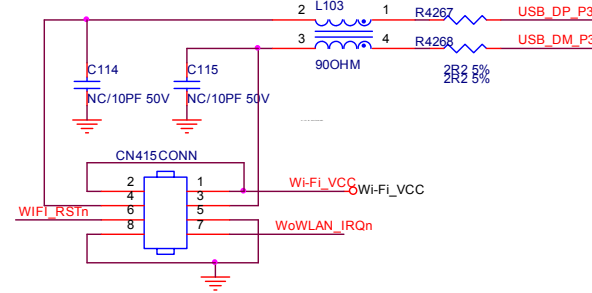
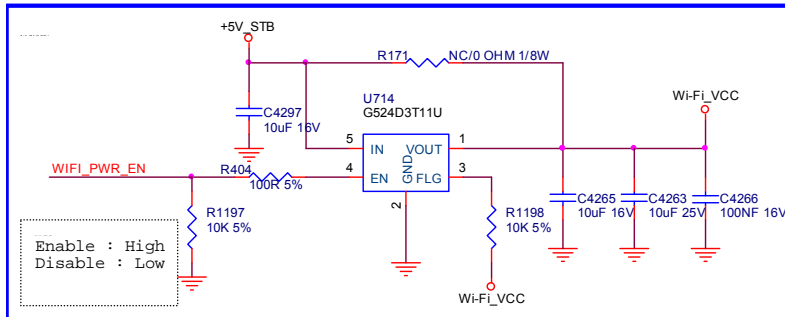
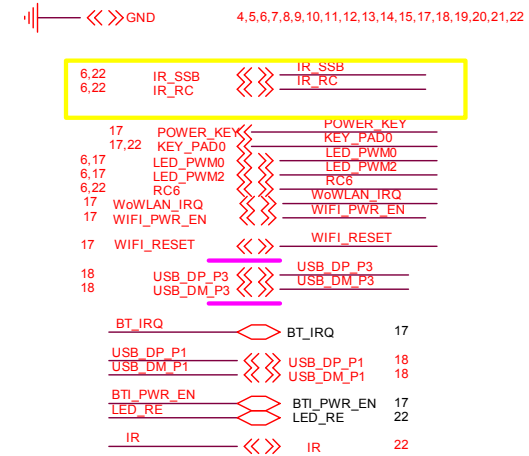
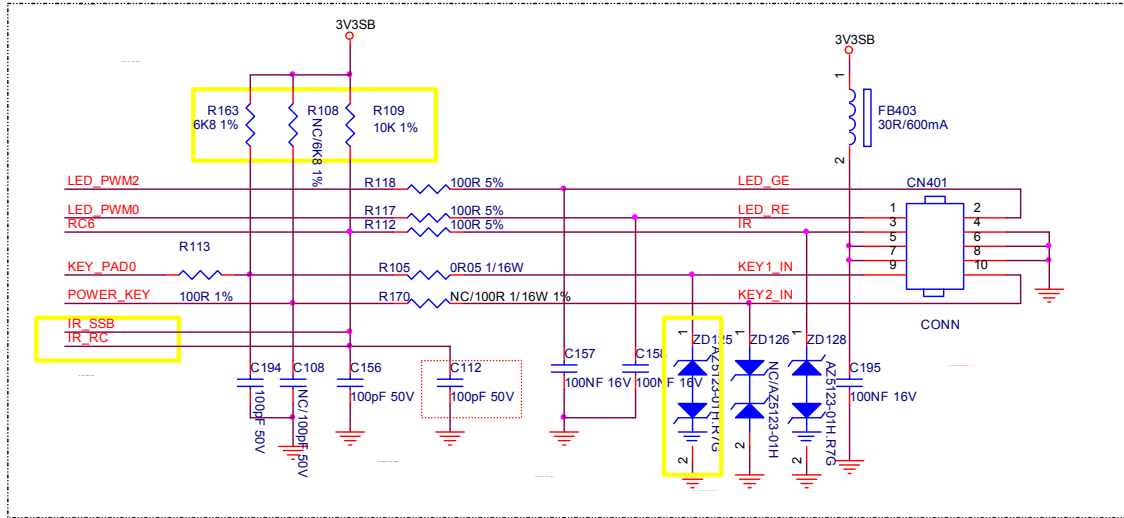
9-3-12 Internal CI



CI_CD1#	R1141	0R05 1/16W	SPI_CLK
CI_CD2#	R1142	0R05 1/16W	SPI_CLK1
CI_IORD#	R1143	0R05 1/16W	SPI_DATA
CI_IOWR#	R1144	0R05 1/16W	SPI_CLE
CI_REG#	R1145	0R05 1/16W	CI_INT
CI_CE1#	R1146	0R05 1/16W	CI_TSCLK
CI_OE#	R1147	0R05 1/16W	CI_TSVAL
CI_WE#	R1148	0R05 1/16W	CI_TSSYNC
CI_IREQ#	R1149	0R05 1/16W	PVR_TSDATA0
CI_WAIT#	R1150	0R05 1/16W	PVR_TSDATA1
CI_VS1#	R1151	0R05 1/16W	PVR_TSVAL
CI_RESET	R1152	0R05 1/16W	CI_RST

CI_A0	CI_A0	17
CI_A1	CI_A1	17
CI_A2	CI_A2	17
CI_A3	CI_A3	17
CI_A4	CI_A4	17
CI_A5	CI_A5	17
CI_A6	CI_A6	17
CI_A7	CI_A7	17
CI_A8	CI_A8	17
CI_A9	CI_A9	17
CI_A10	CI_A10	17
CI_A11	CI_A11	17
CI_A12	CI_A12	17
CI_A13	CI_A13	17
CI_A14	CI_A14	17
CI_MCLKI	CI_MCLKI	17
CI_MIVAL	CI_MIVAL	17
CI_MISTR	CI_MISTR	17
CI_MIVAL	CI_MIVAL	17
CI_MD10	CI_MD10	17
CI_MD11	CI_MD11	17
CI_MD12	CI_MD12	17
CI_MD13	CI_MD13	17
CI_MD14	CI_MD14	17
CI_MD15	CI_MD15	17
CI_MD16	CI_MD16	17
CI_MD17	CI_MD17	17
CI_D0	CI_D0	17
CI_D1	CI_D1	17
CI_D2	CI_D2	17
CI_D3	CI_D3	17
CI_D4	CI_D4	17
CI_D5	CI_D5	17
CI_D6	CI_D6	17
CI_D7	CI_D7	17
CI_MDO0	CI_MDO0	17
CI_MDO1	CI_MDO1	17
CI_MDO2	CI_MDO2	17
CI_MDO3	CI_MDO3	17
CI_MDO4	CI_MDO4	17
CI_MDO5	CI_MDO5	17
CI_MDO6	CI_MDO6	17
CI_MDO7	CI_MDO7	17
SPI_CLK	SPI_CLK	17
SPI_CLK1	SPI_CLK1	17
SPI_DATA	SPI_DATA	17
SPI_CLE	SPI_CLE	17
CI_INT	CI_INT	17
CI_TSCLK	CI_TSCLK	17
CI_TSVAL	CI_TSVAL	17
CI_TSSYNC	CI_TSSYNC	17
CI_TSDATA0	CI_TSDATA0	17
PVR_TSSYNC	PVR_TSSYNC	17
PVR_TSCLK	PVR_TSCLK	17
PVR_TSDATA0	PVR_TSDATA0	17
PVR_TSDATA1	PVR_TSDATA1	17
PVR_TSVAL	PVR_TSVAL	17
CI_RST	CI_RST	17

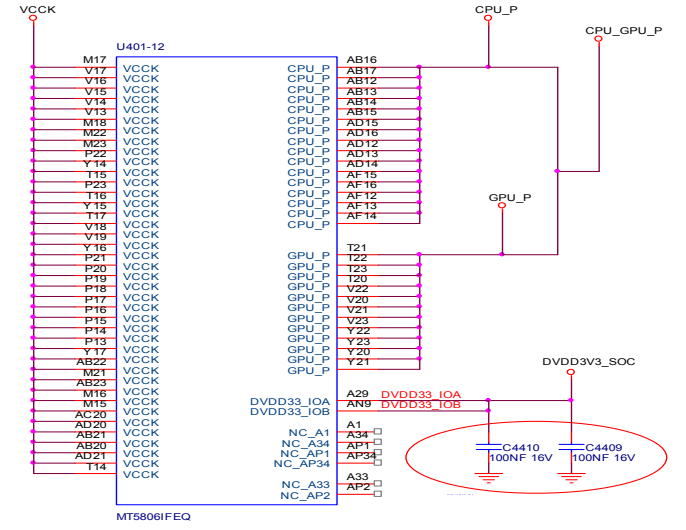
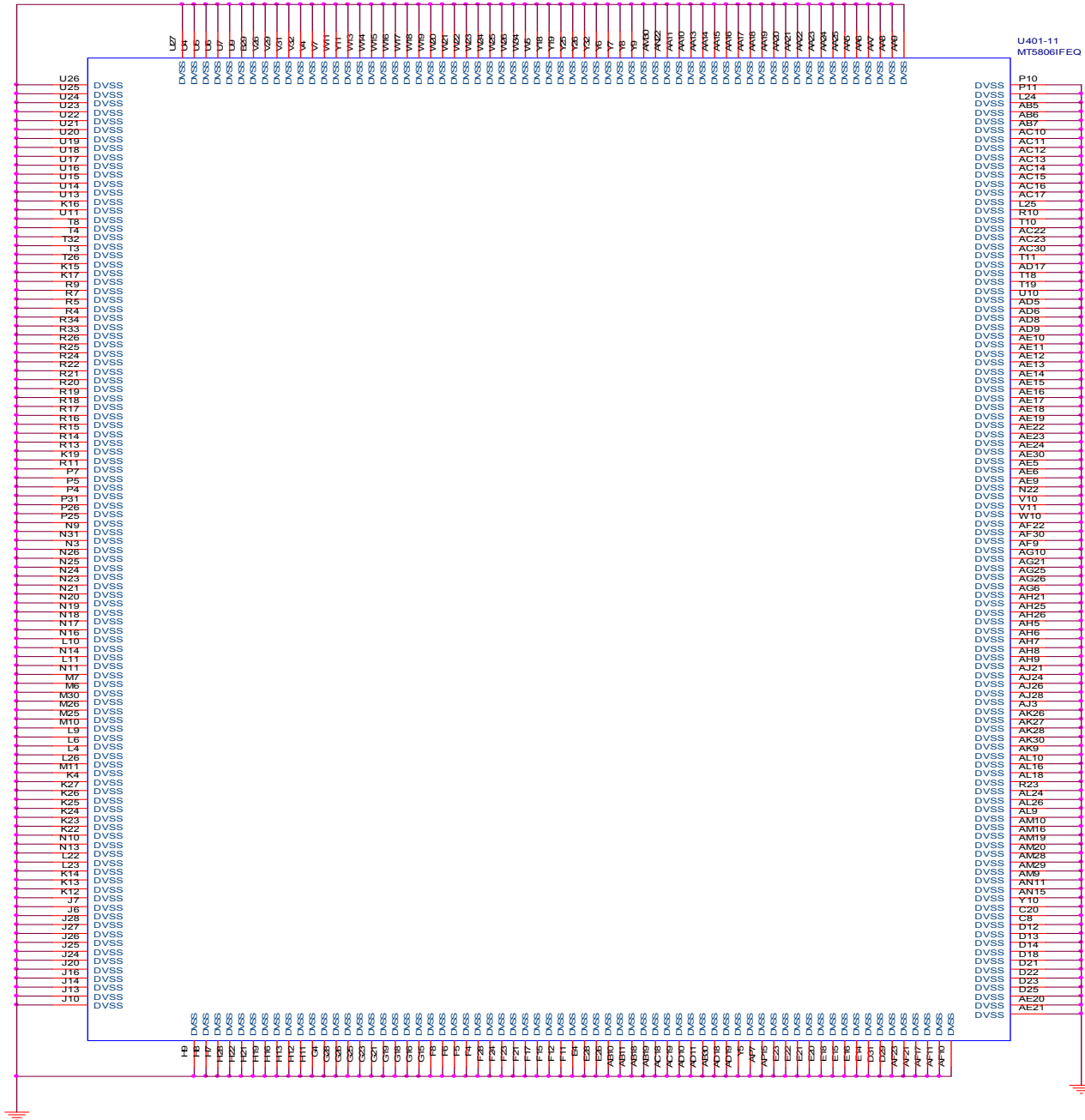
9-3-13 WIFI/KEYPAD



9-3-14 GPIO/ServAD

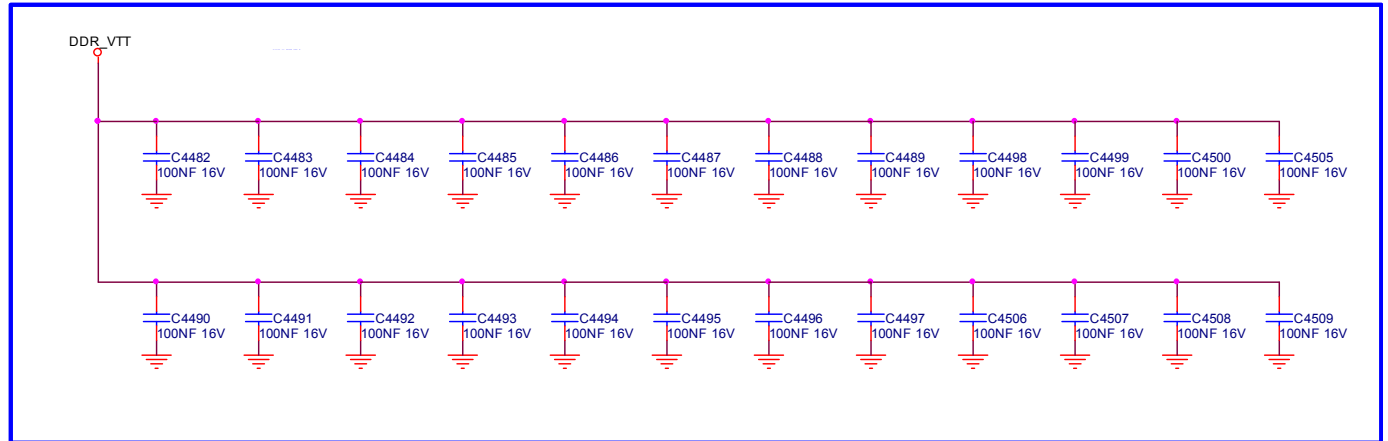
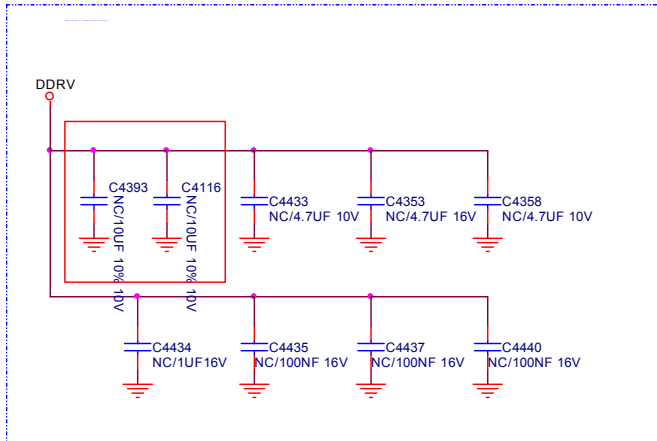
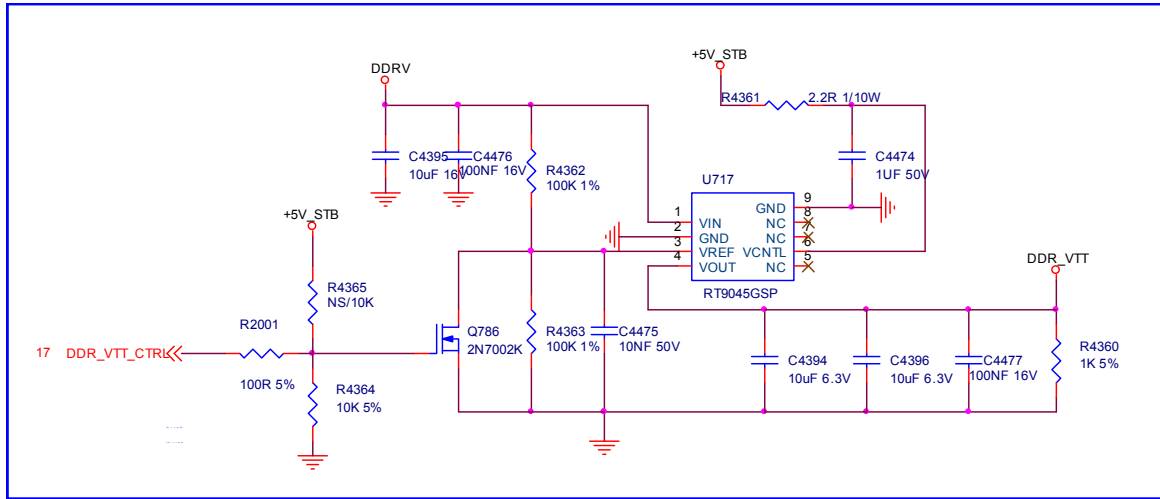


9-3-16 VCCK & DVSS



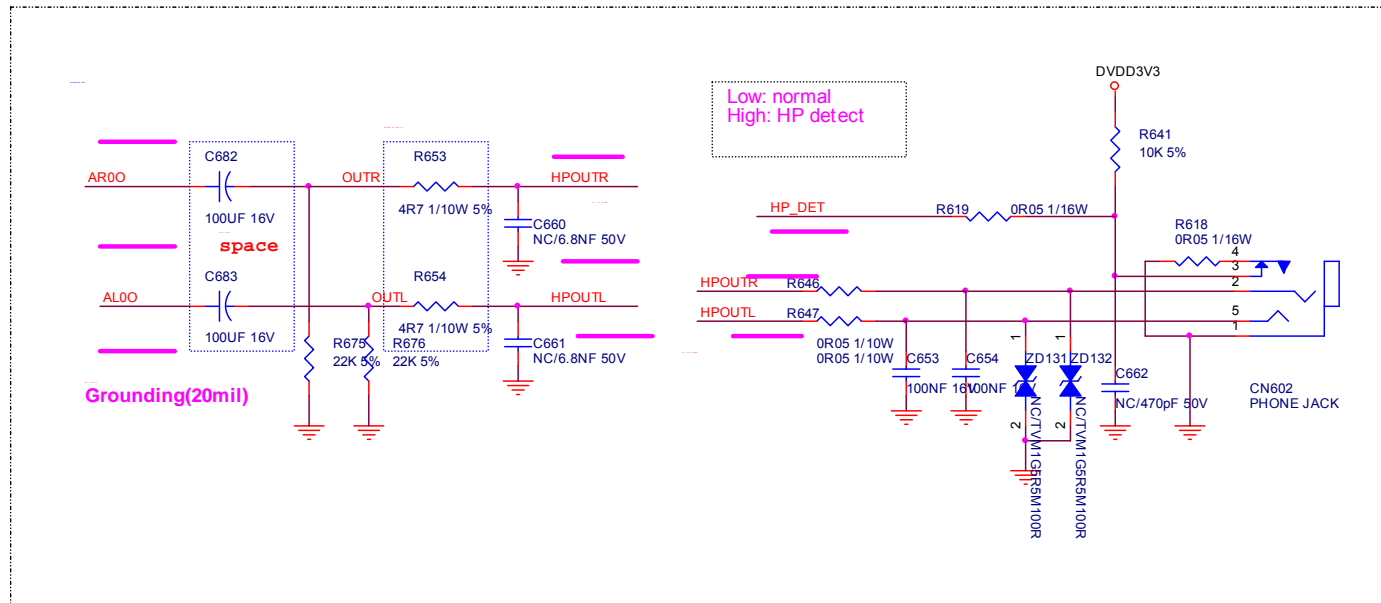
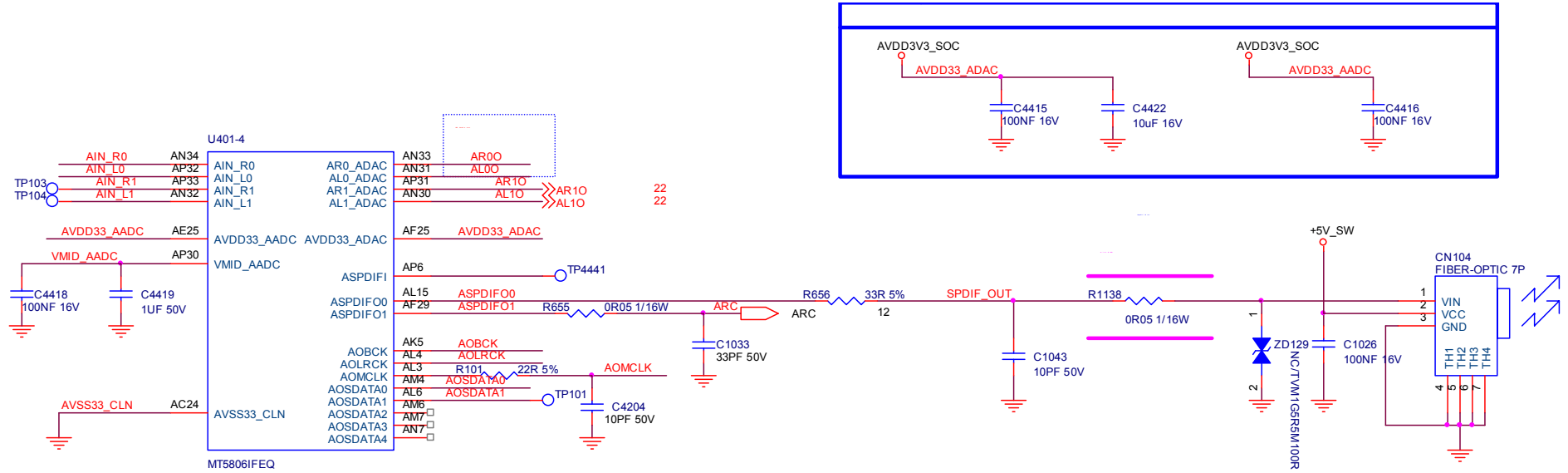
4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,20,21,22

9-3-17 DDP POWER



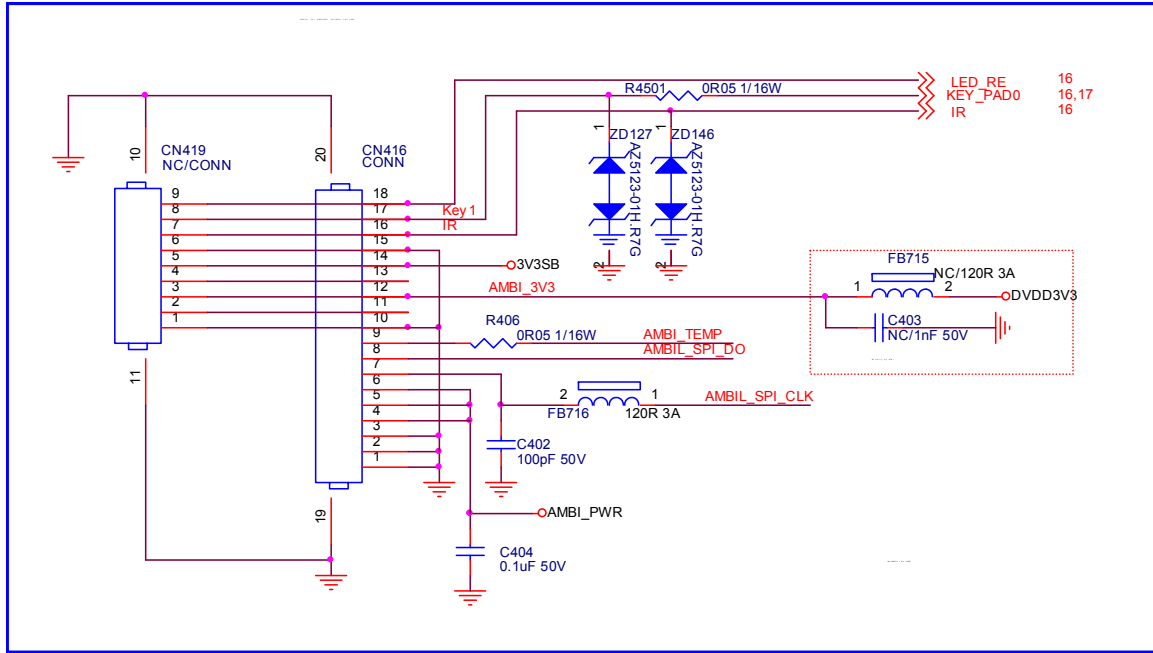
4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 21, 22

9-3-18 HP AND SPDIF

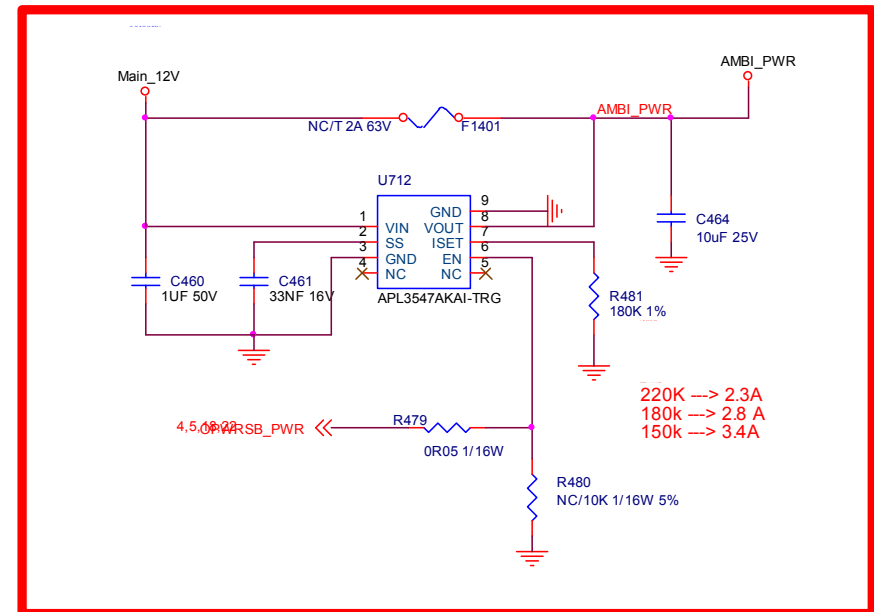
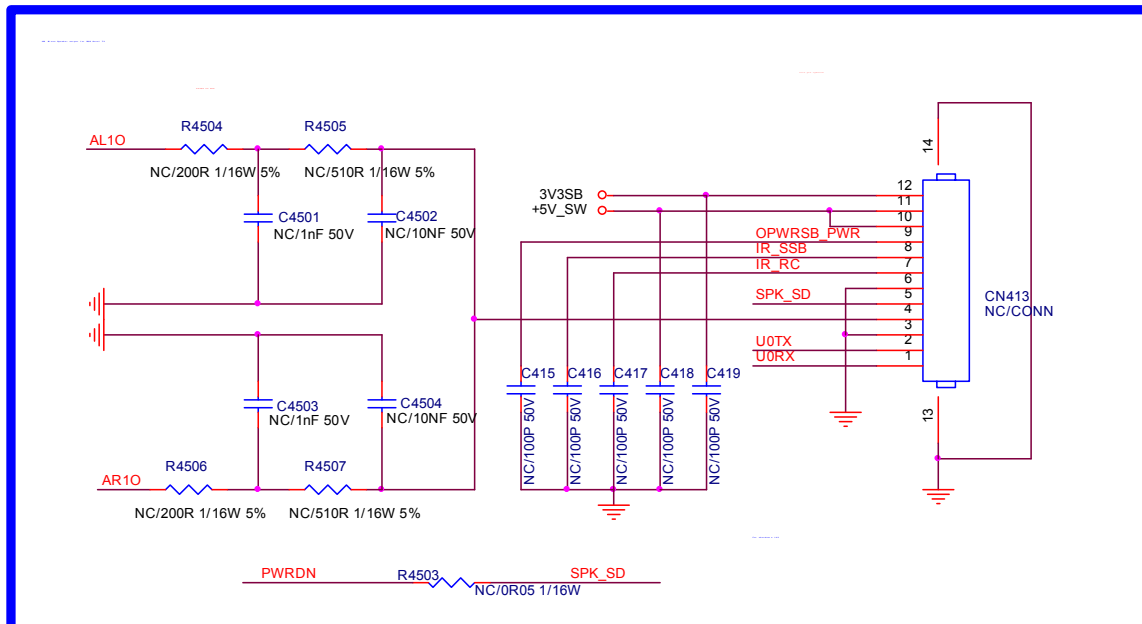
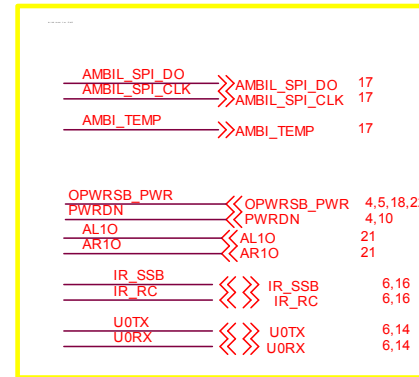


AOMCLK	<<>>	AOMCLK	10
AOBCK	<<>>	AOBCK	10
AOLRCK	<<>>	AOLRCK	10
AOSDATA0	<<>>	AOSDATA0	10
HP_DET	<<>>	HP_DET	17
AIN_R0	<<>>	AIN_R0	9,14
AIN_L0	<<>>	AIN_L0	9,14
GND	<<>>	GND	4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,22

9-3-19 AMBILIGHT & Hotel TV

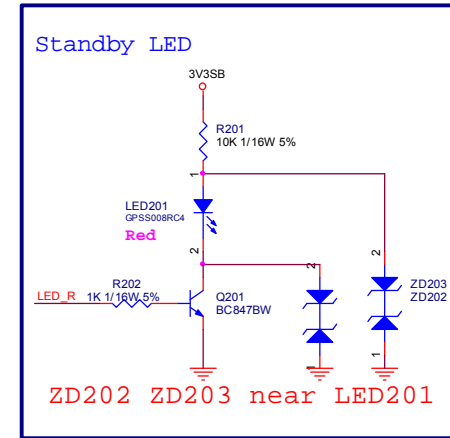
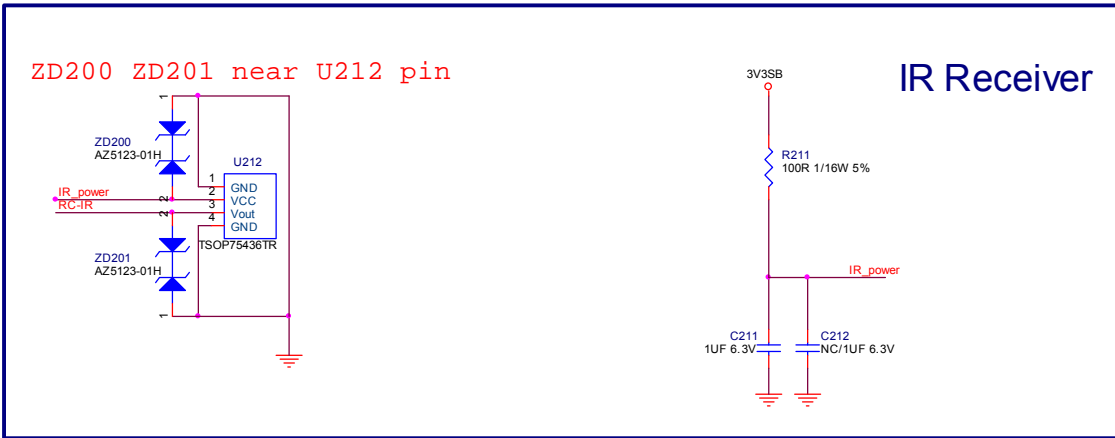
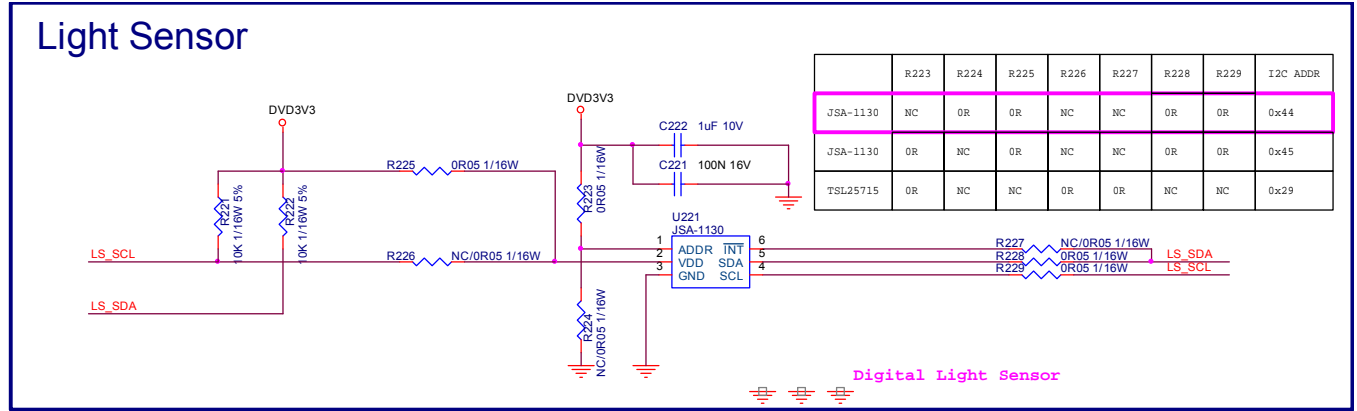
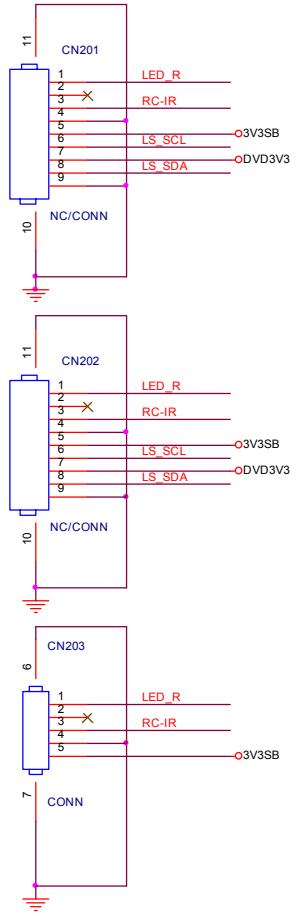


⏏ GND 4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21



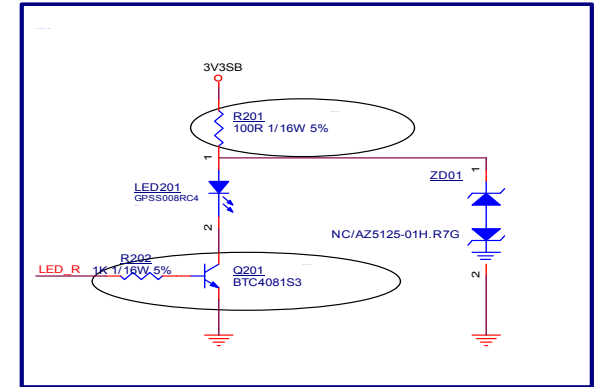
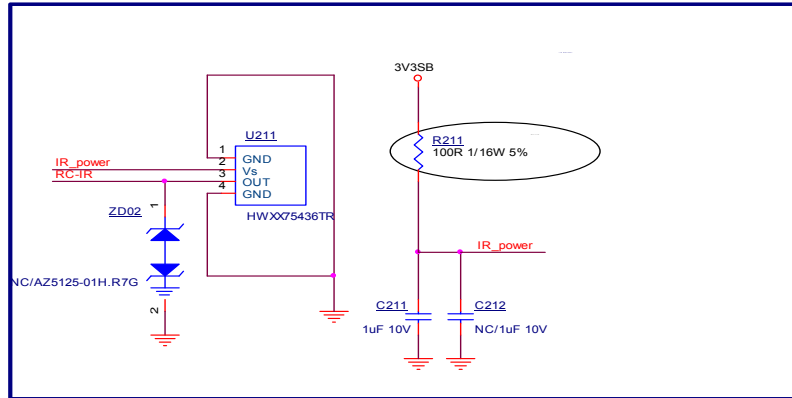
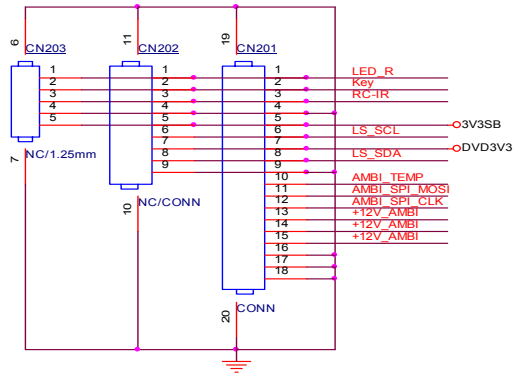
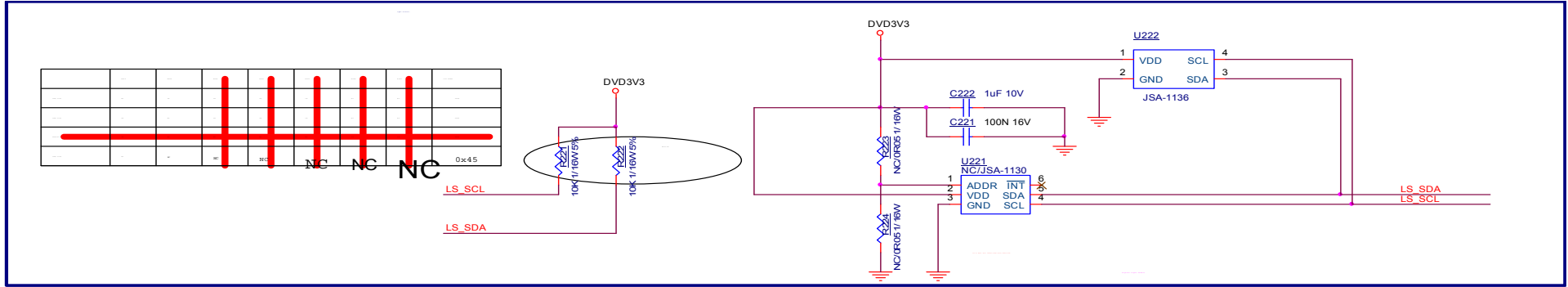
9.4 E 715G8623 IR Board (For 43"50" 6504 Series)

9-4-1 IR

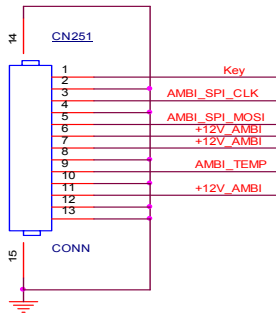


9.5 E 715GA039 IR Board (For 43"50" 6704 Series)

9-5-1 IR&STB LED&Light sensor



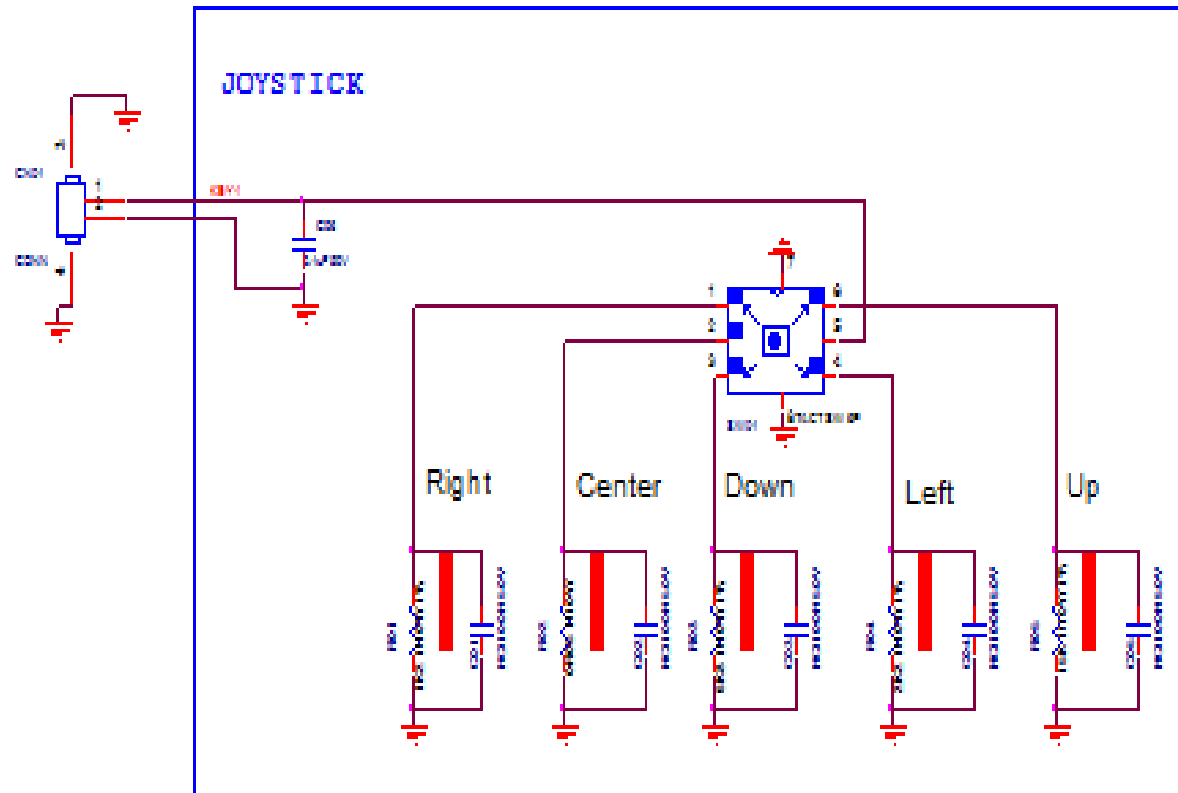
- Key
- AMBI_TEMP
- AMBI_SPI_MOSI
- AMBI_SPI_CLK
- +12V_AMBI



- LED_R TP1
- Key TP2
- RC-IR TP3
- 3V3SB TP4
- DVD3V3 TP5
- LS_SCL TP6
- LS_SDA TP7
- TP8

9.6 E 715G9740 Keyboard control panel (For 43"50" 6504/6704 Series)

9-6-1 Key



Direction	switch	Key function	Resistance	Voltage	Range
Center	2-5 short	Menu	0R	0V	0 to 0.27V
Right	1-5 short	CH+	10K	0.9V	0.29 to 0.80 V
Left	4-5 short	CH-	2K	0.51V	0.67 to 0.95 V
Down	3-5 short	VOL-	5K	1.65V	1.41 to 1.87 V
Up	6-5 short	VOL+	15K	2.27V	1.93 to 2.95 V
		No function		2.2V	2.125 to 2.485 V

Joystick circuit diagram

	pin1	pin2	pin3	pin4	pin5	pin6
F1		○	○	○		
F2				○	○	
F3			○	○		
F4					○	○
F5	○	○	○	○		