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Internal Use Only

<http://aic.lgservice.com>
<http://eic.lgservice.com>
<http://biz.lgservice.com>

LED TV

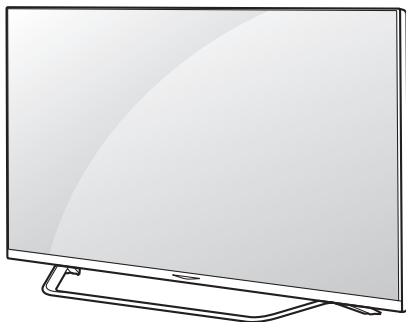
SERVICE MANUAL

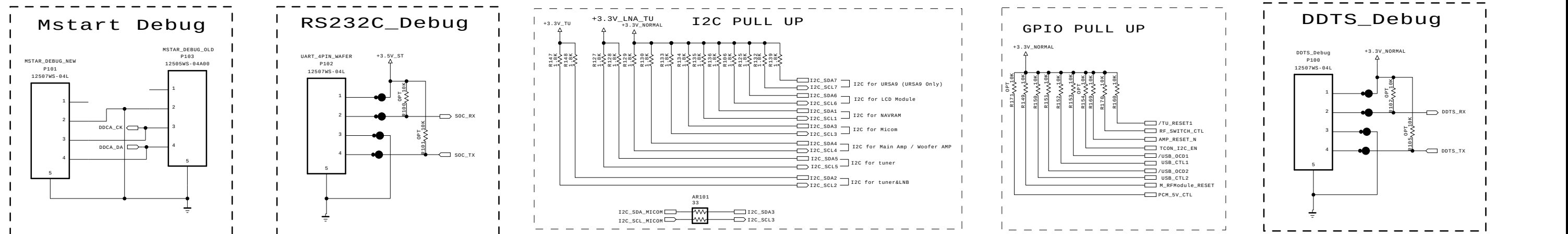
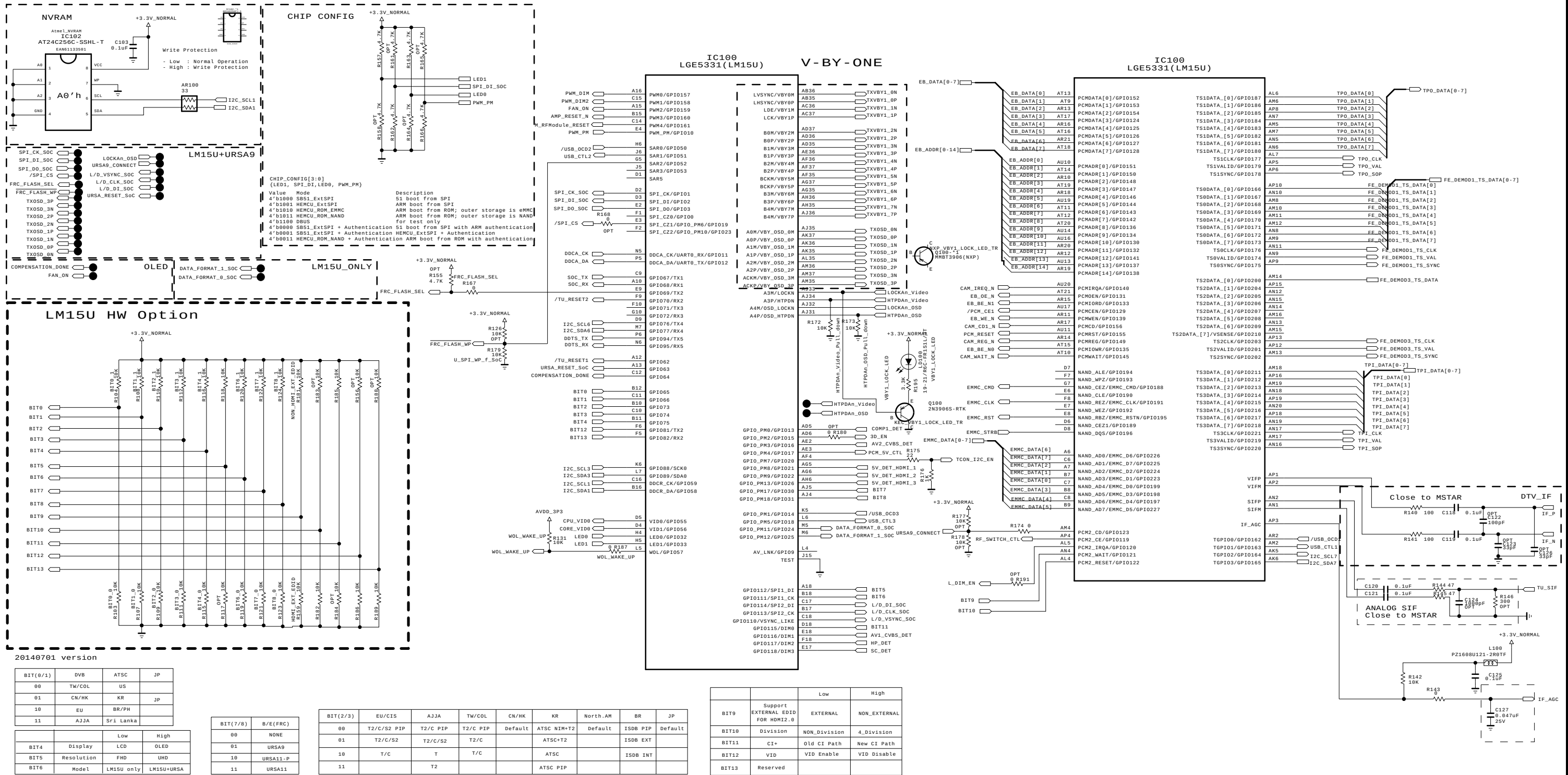
CHASSIS : LD53H / LD53J

MODEL : 55UF85** 55UF85**-Z*

CAUTION

BEFORE SERVICING THE CHASSIS,
READ THE SAFETY PRECAUTIONS IN THIS MANUAL.



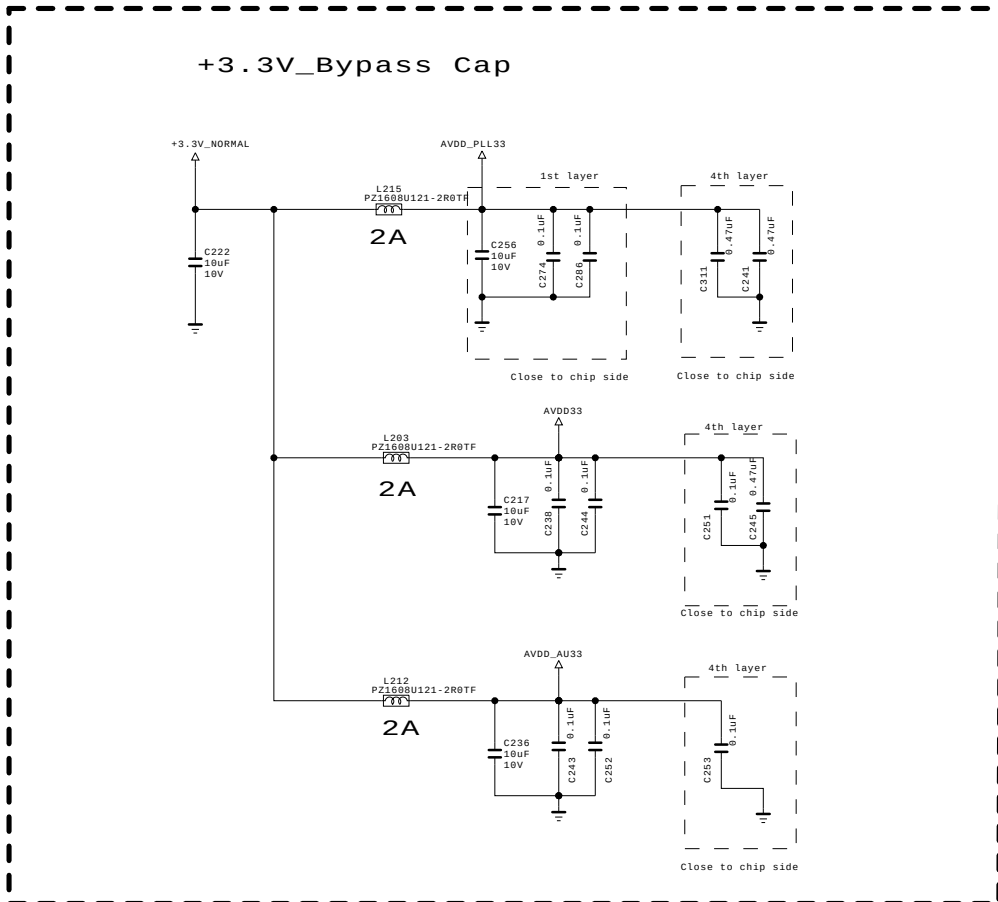
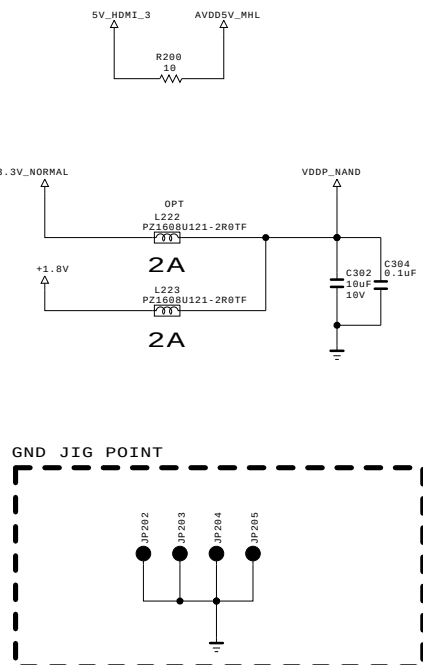
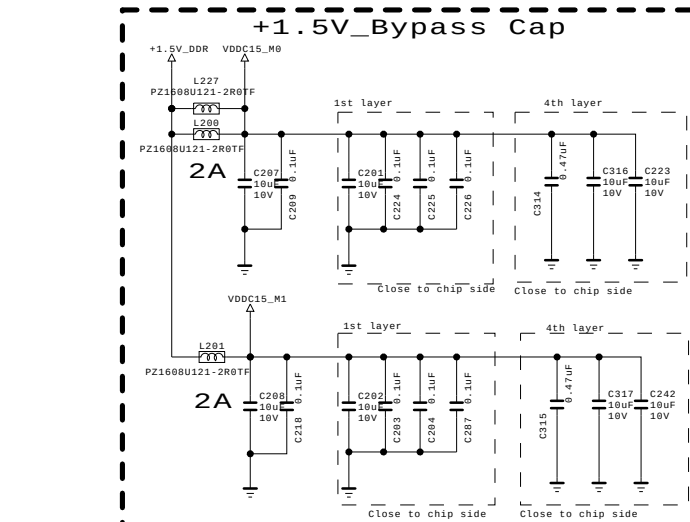


THE SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FIRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMATIC.

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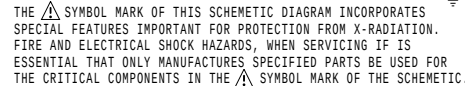
MODEL	LM15U	DATE	2014-12-17
BLOCK	MAIN1_SYSTEM	SHEET	1

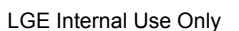
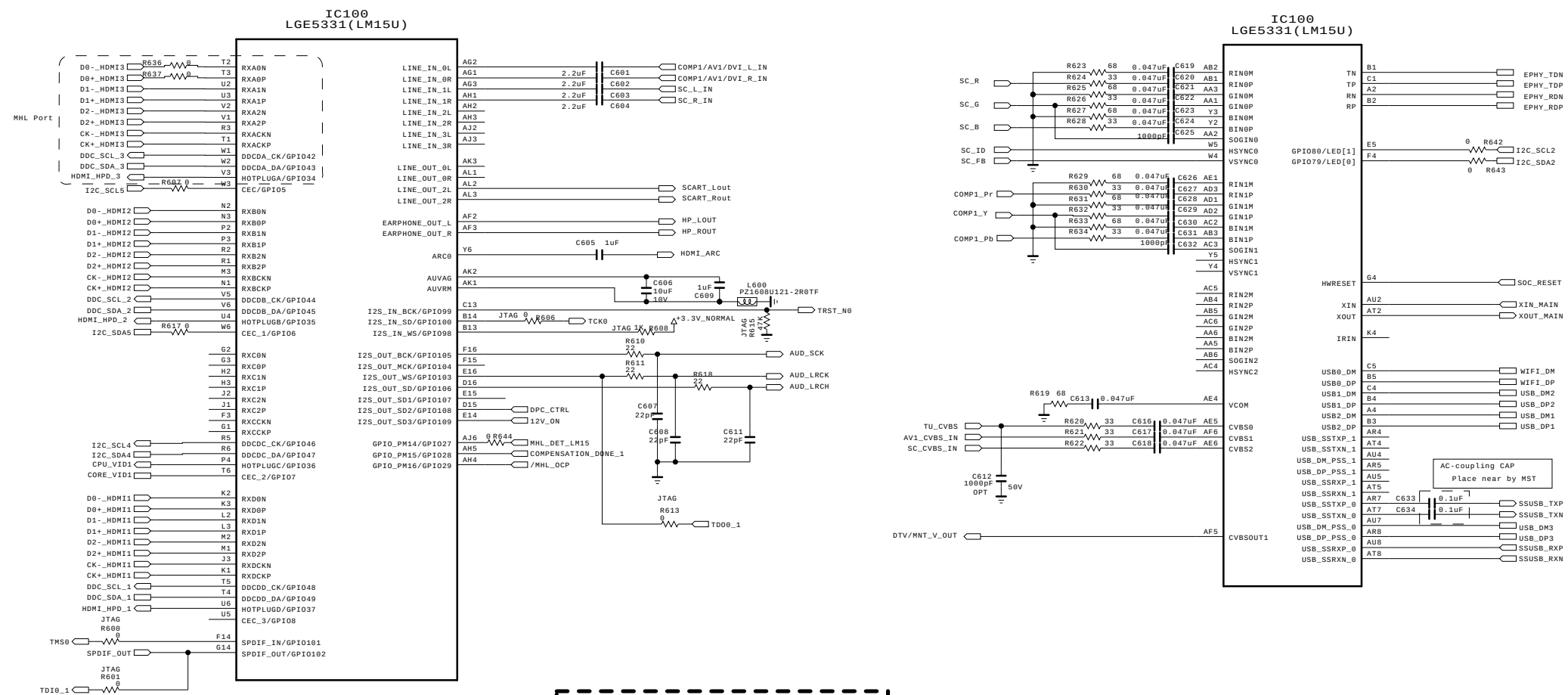
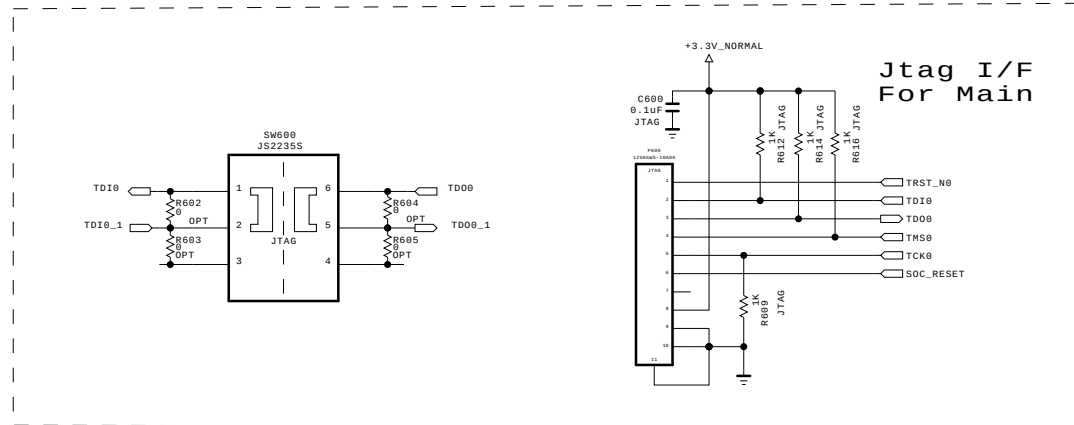


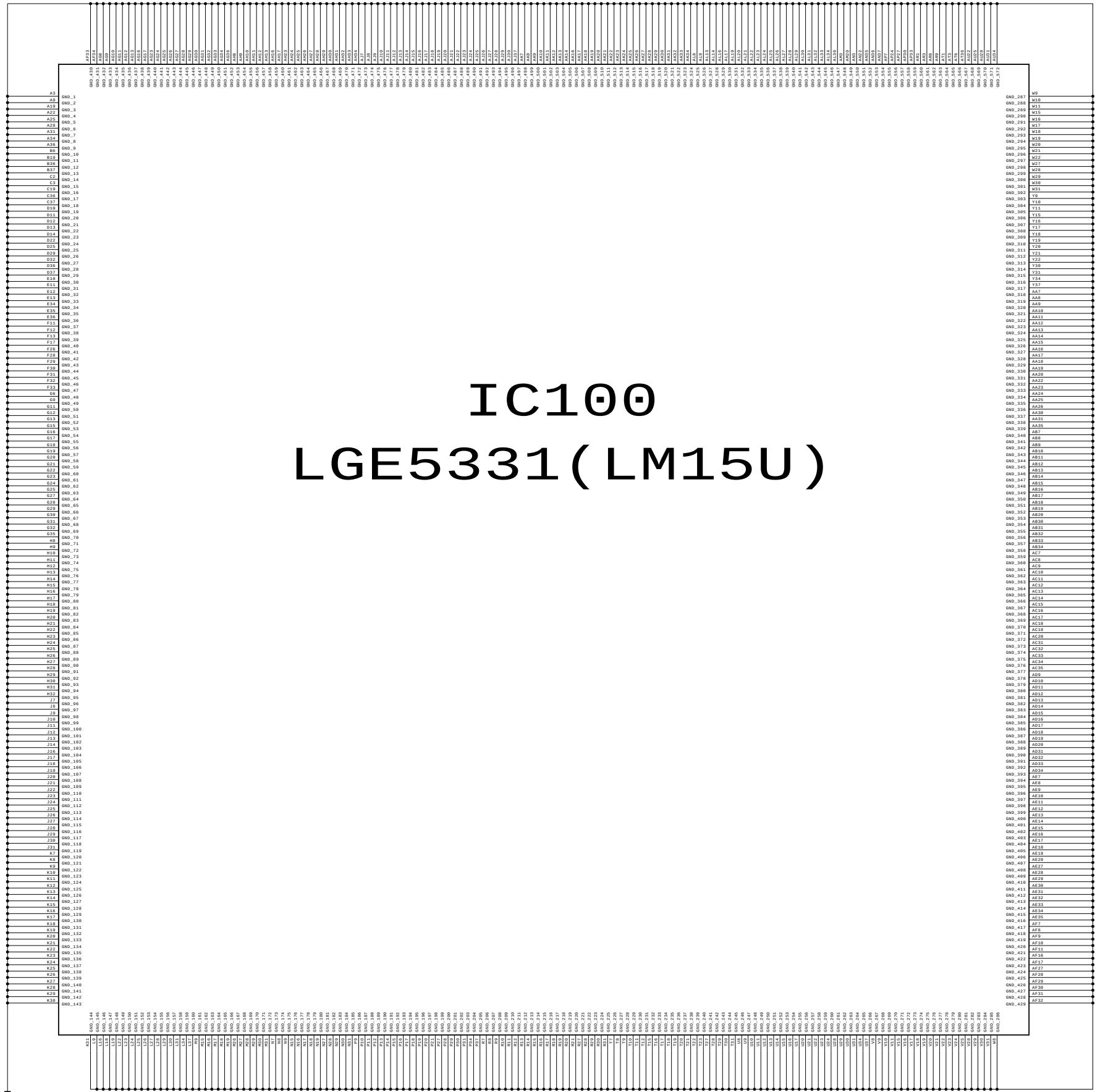
SECRET
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MODEL	LM15U	DATE	2014-11-26
BLOCK	MAIN2_POWER	SHEET	2 /







THE  SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FIRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMATIC.

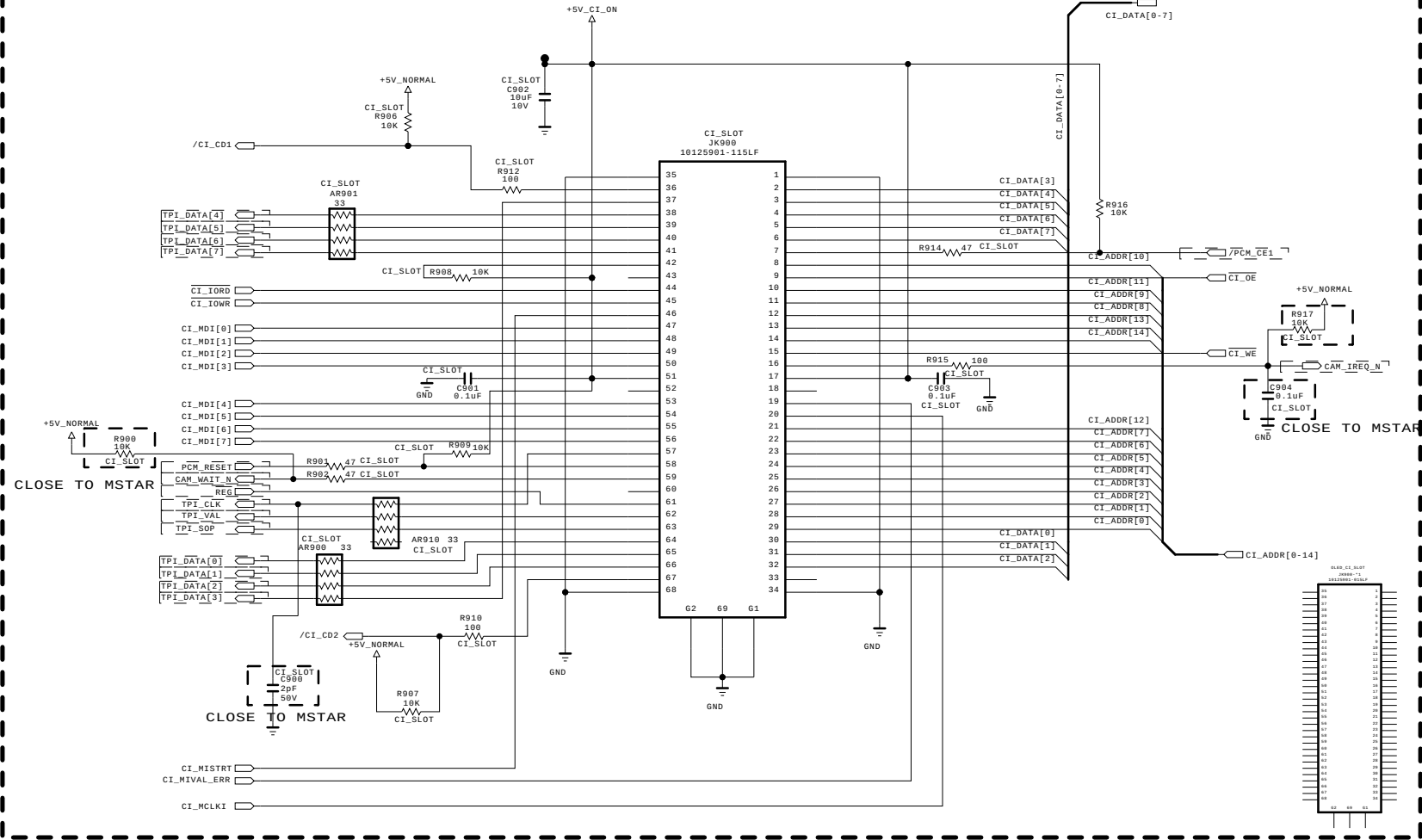
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LGElectronics



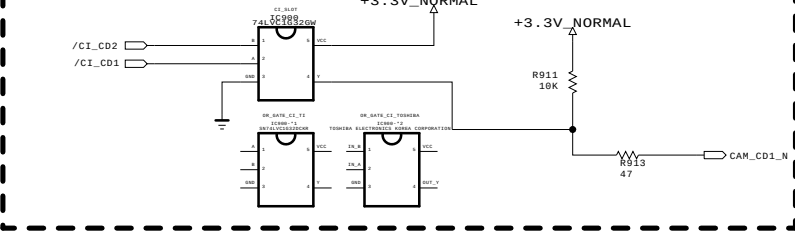
MODEL	LM15U	DATE	2014-1-21
BLOCK	LM15U_GND	SHEET	05

CI Region

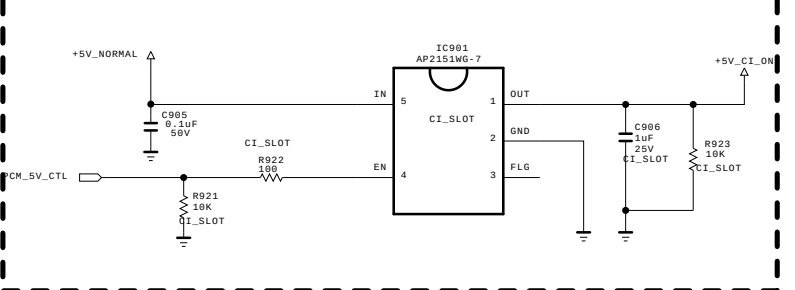
CI SLOT



CI DETECT

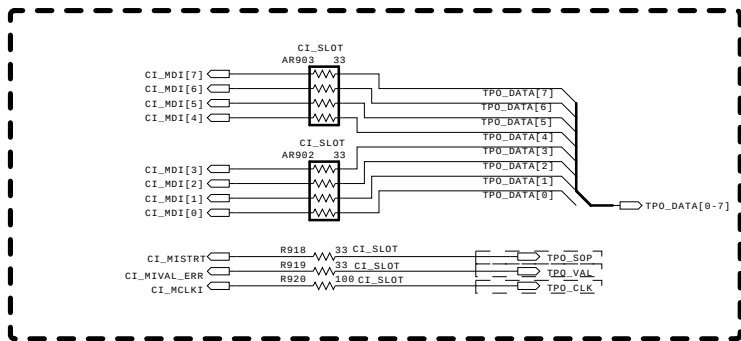


CI POWER ENABLE CONTROL

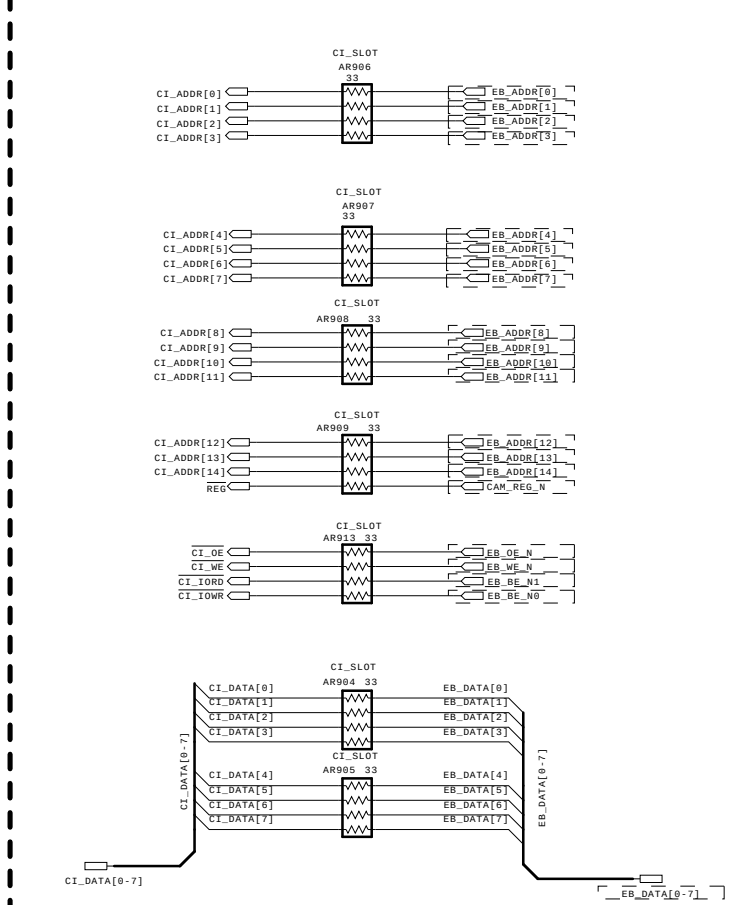


* Option name of this page : CI_SLOT (because of Hong Kong)

CI TS INPUT



CI HOST I/F

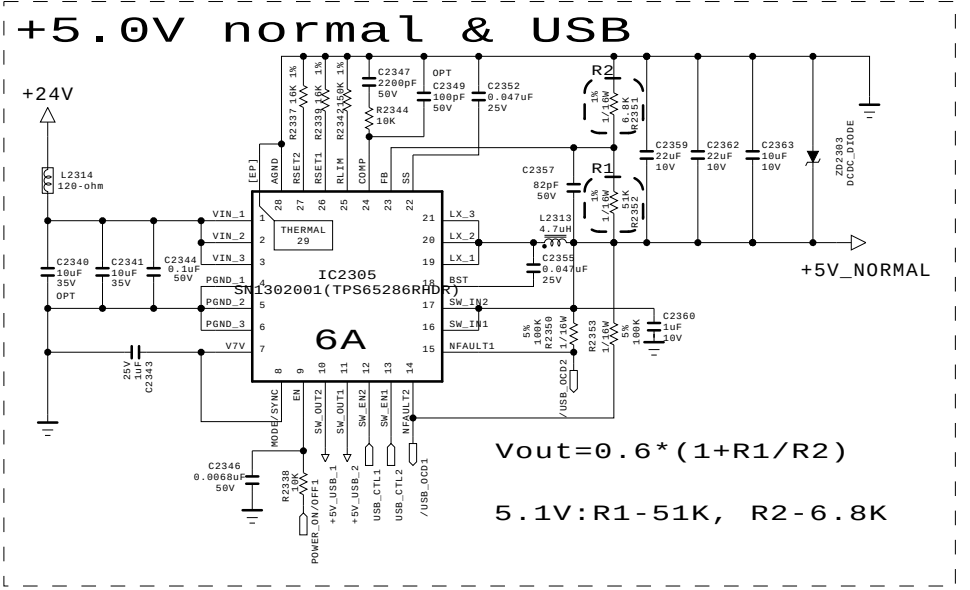
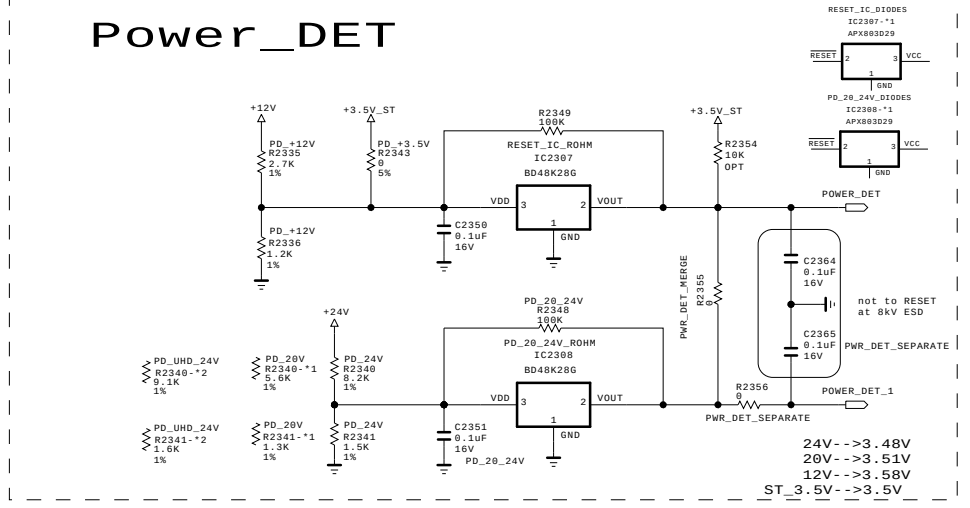
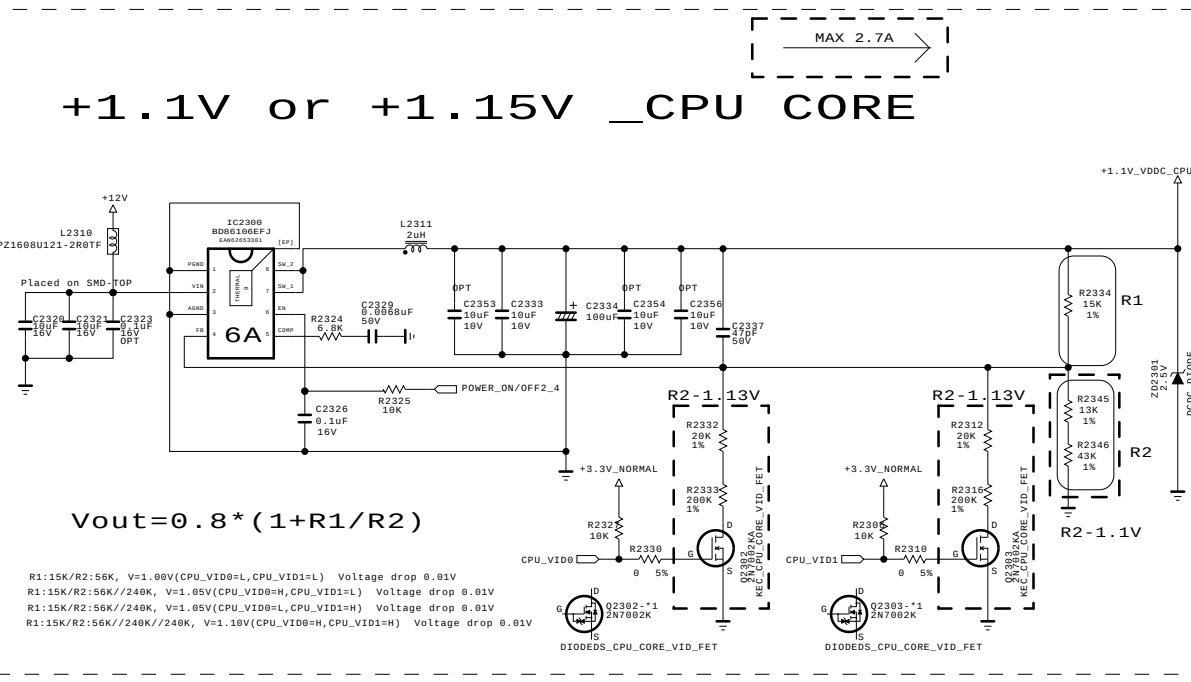
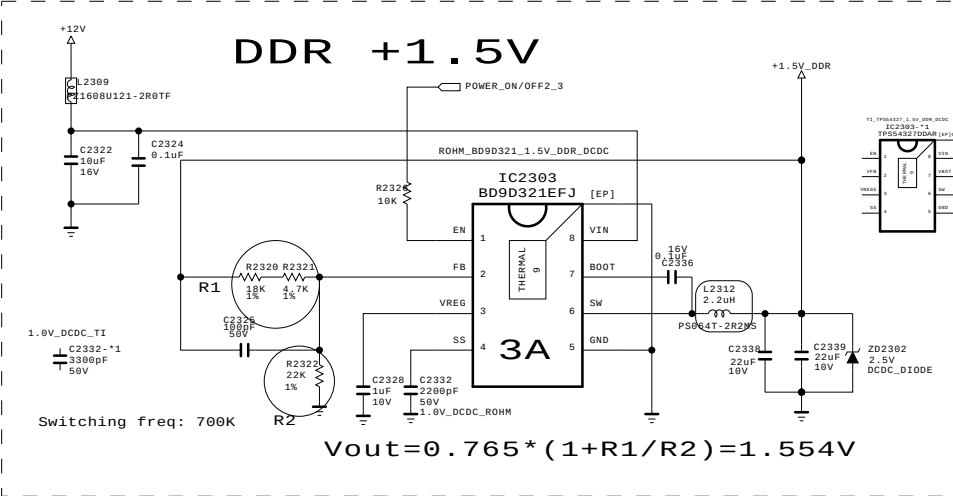
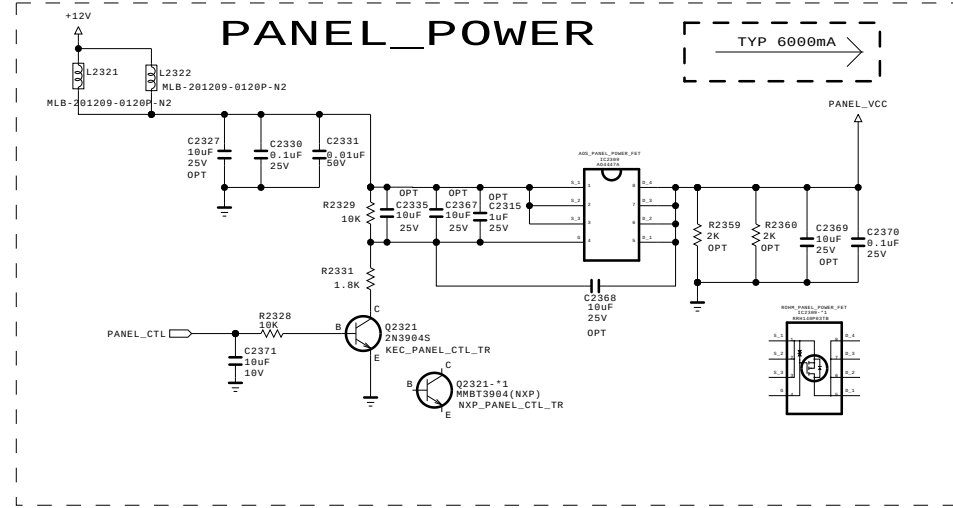
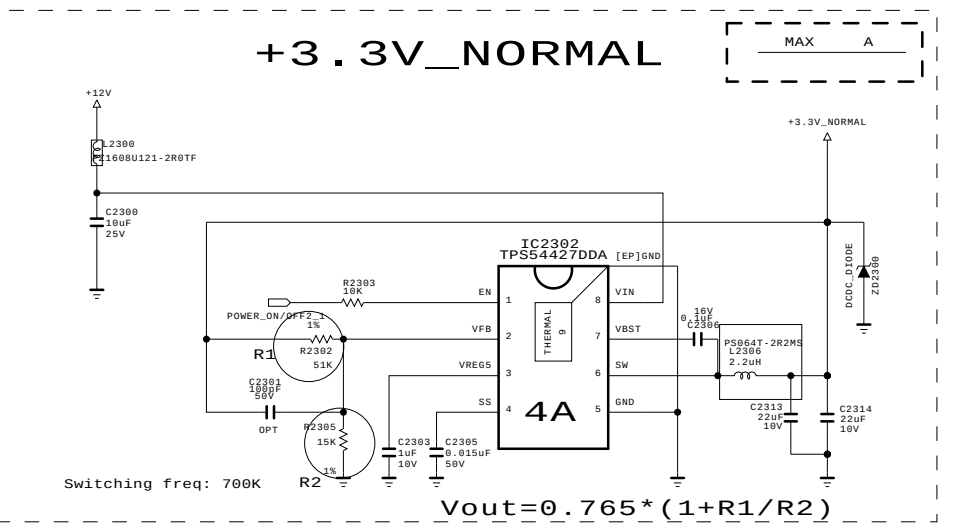
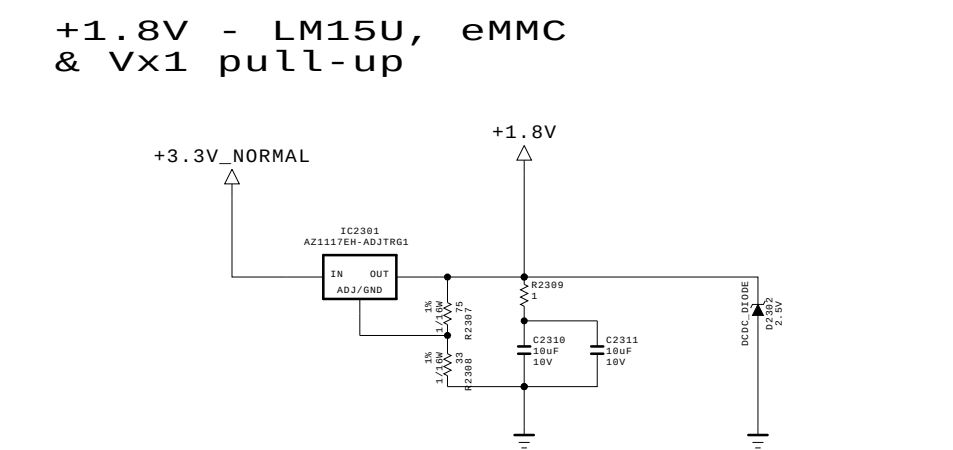
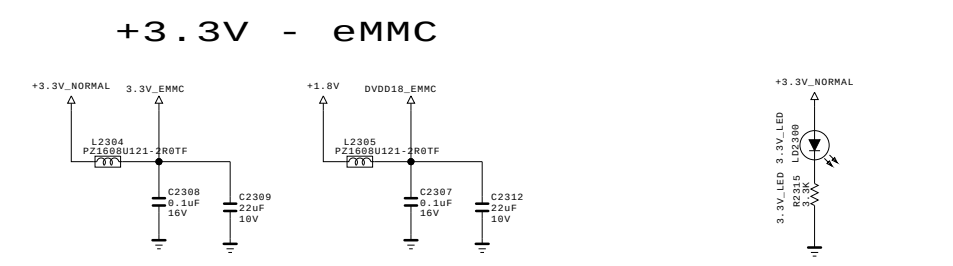
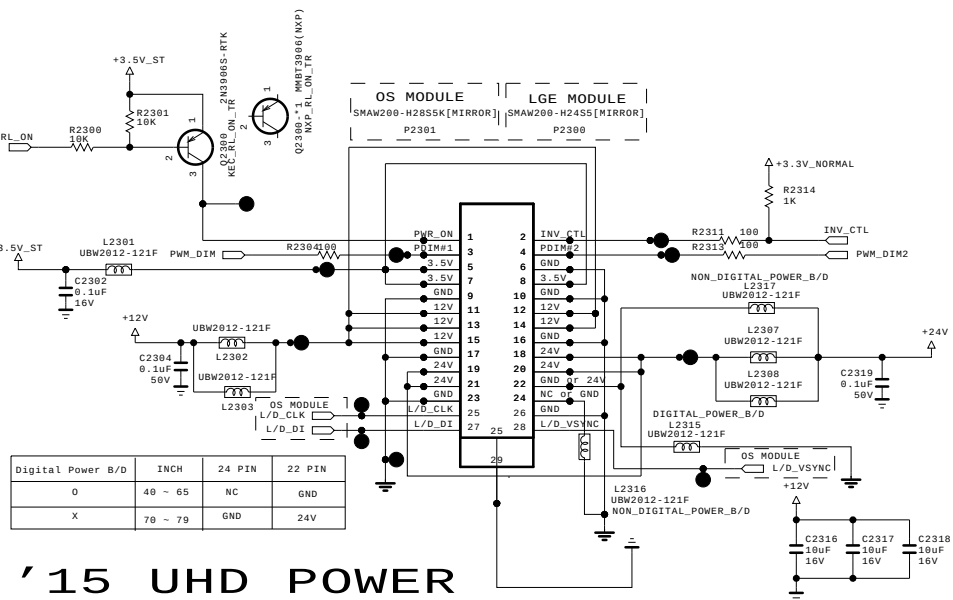


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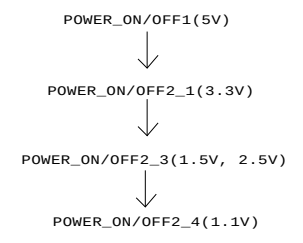
SECRET
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MODEL	LM15U	DATE	2014-10-17
BLOCK	PCMC I	SHEET	9



LM15 Power SEQUENCE

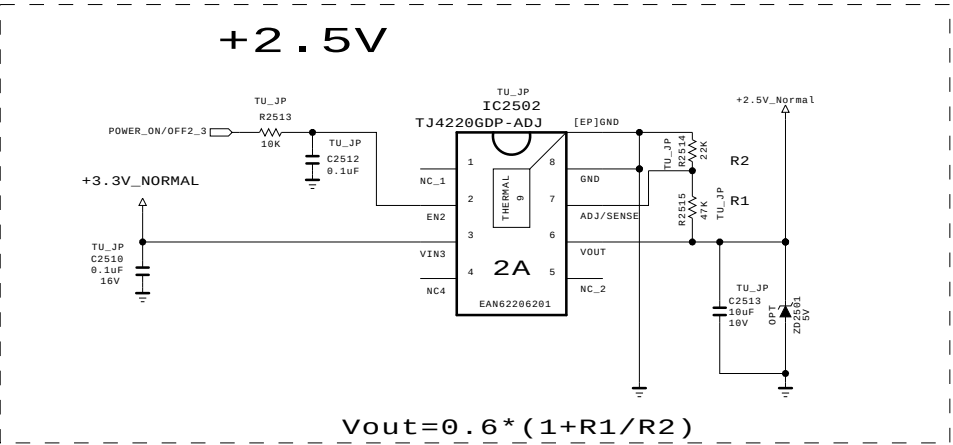
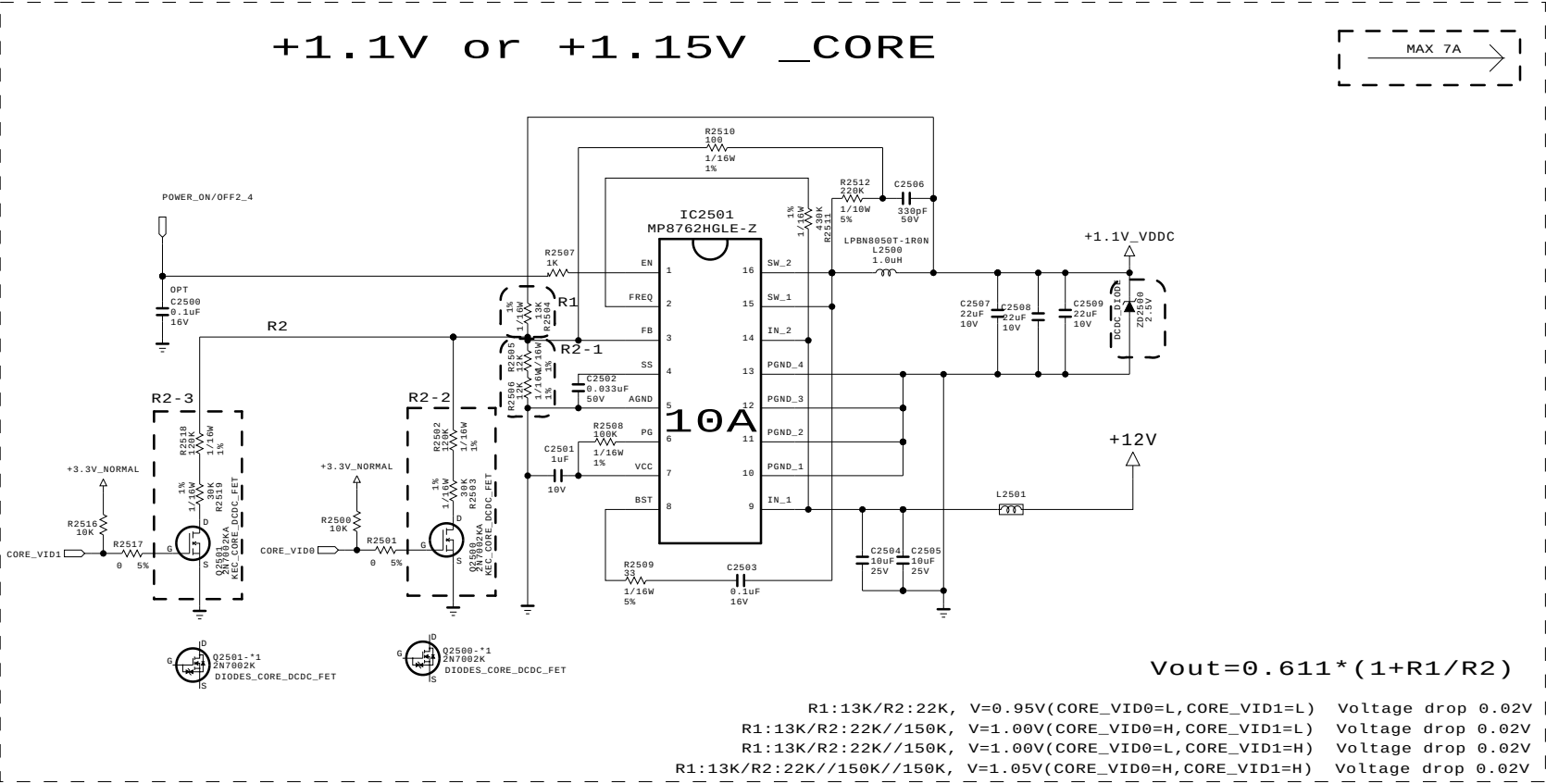


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MODEL	LM15U	DATE	2014-12-15
BLOCK	LM15U_PWR_1_UHD	SHEET	23



THE  SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FIRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMATIC.

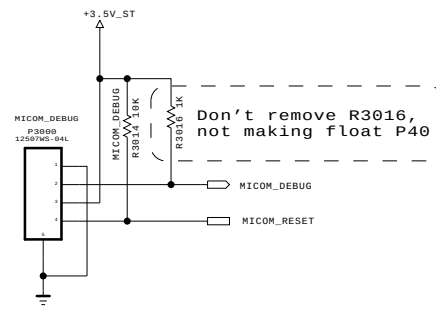
SECRET
LGElectronics

 LG ELECTRONICS

MODEL	LM15U	DATE	2014-11-26
BLOCK	LM15U_PWR_2_ALL	SHEET	25

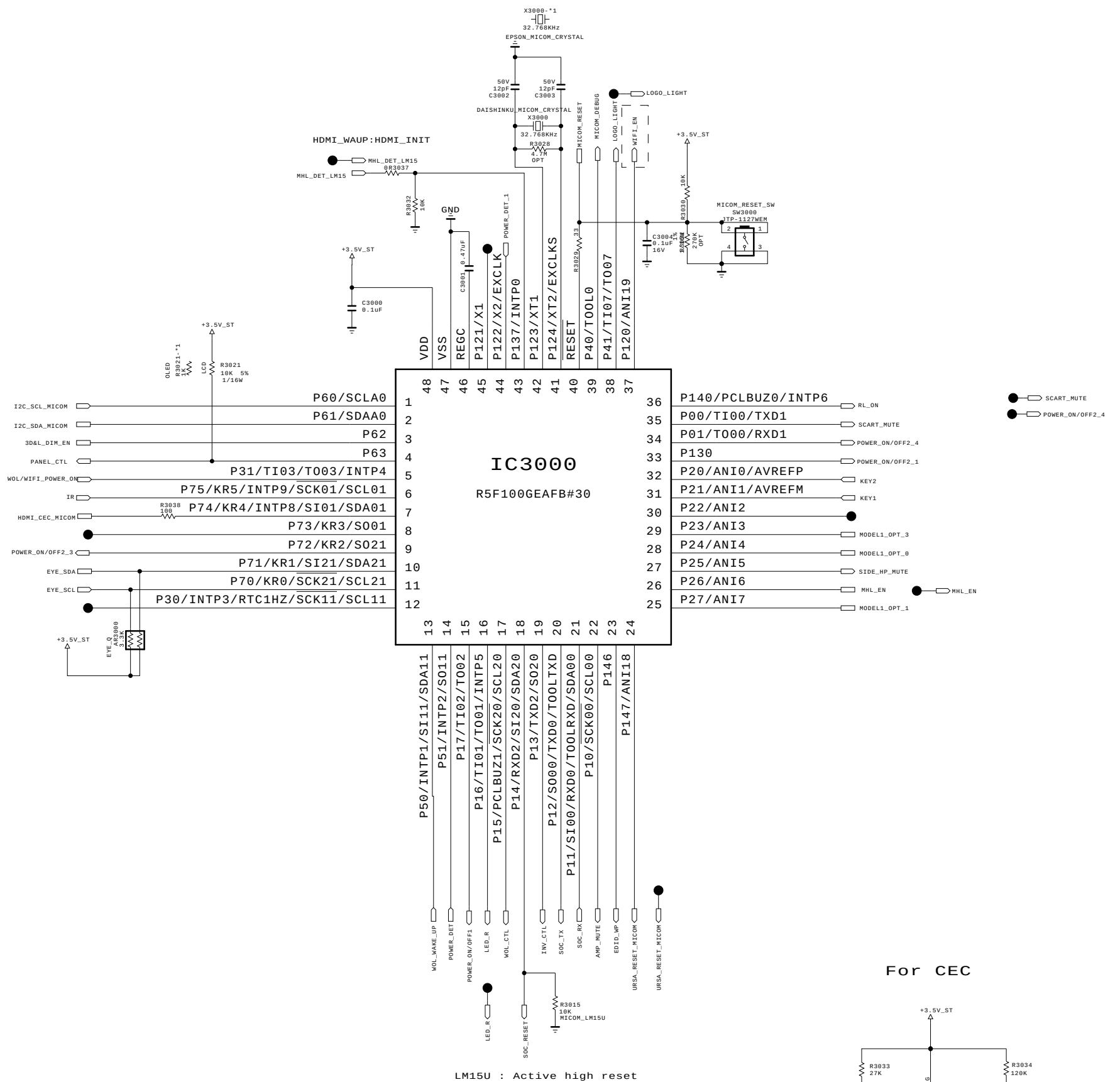
For Debug

The diagram illustrates the connection of the P3090 microcontroller for debugging. The chip is shown with pins 1 through 5. Pin 1 is connected to a +3.5V_ST supply. Pin 2 is connected to a 30K resistor (R3014). Pin 3 is connected to a 10K resistor (R3015). Pin 4 is connected to ground. Pin 5 is connected to ground. A dashed box highlights the connection to R3014 and R3015, with a note: "Don't remove R3016, not making float P40". The output of the P3090 is connected to MICOM_DEBUG and MICOM_RESET.



```

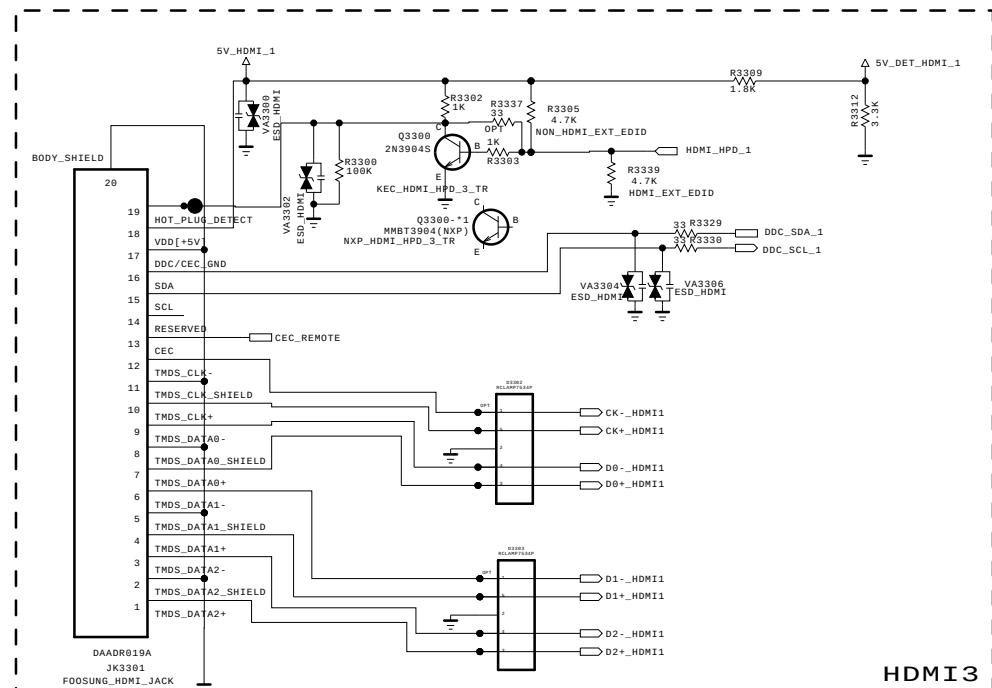
graph TD
    A[POWER_ON/OFF1(5V)] --> B[POWER_ON/OFF2_1(3.3V)]
    B --> C[POWER_ON/OFF2_3(1.5V)]
    C --> D[POWER_ON/OFF2_4(1.1V)]
    D --> E[SOC_RESET]
  
```



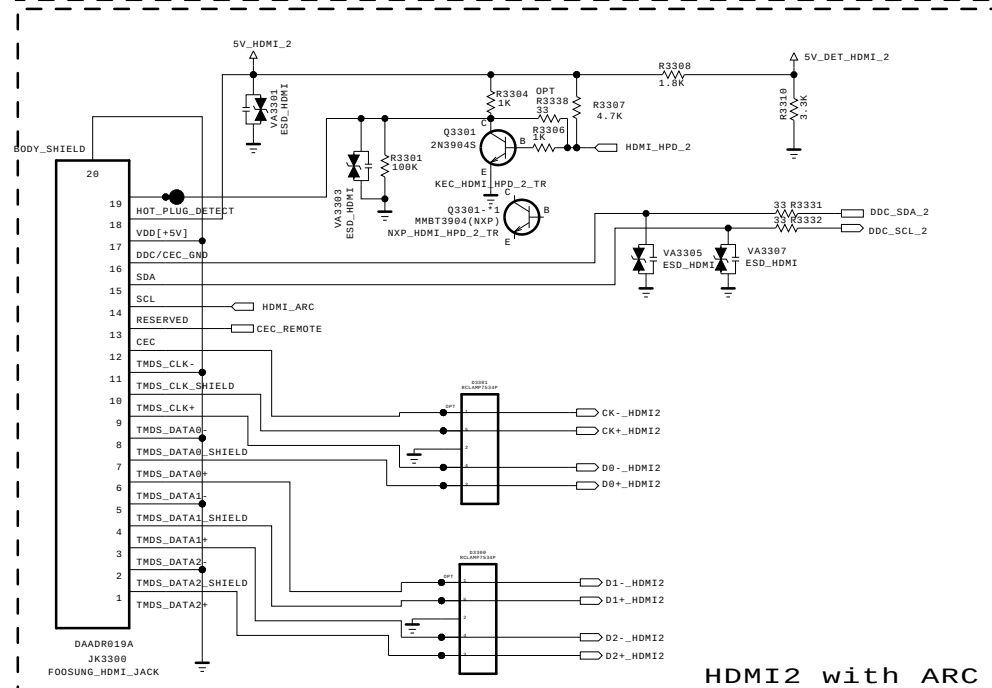
	0	1	
MODEL_OPT_0	NON LOGO	LOGO	
MODEL_OPT_1	LCD	OLED	
MODEL_OPT_3	LM15U	H15	

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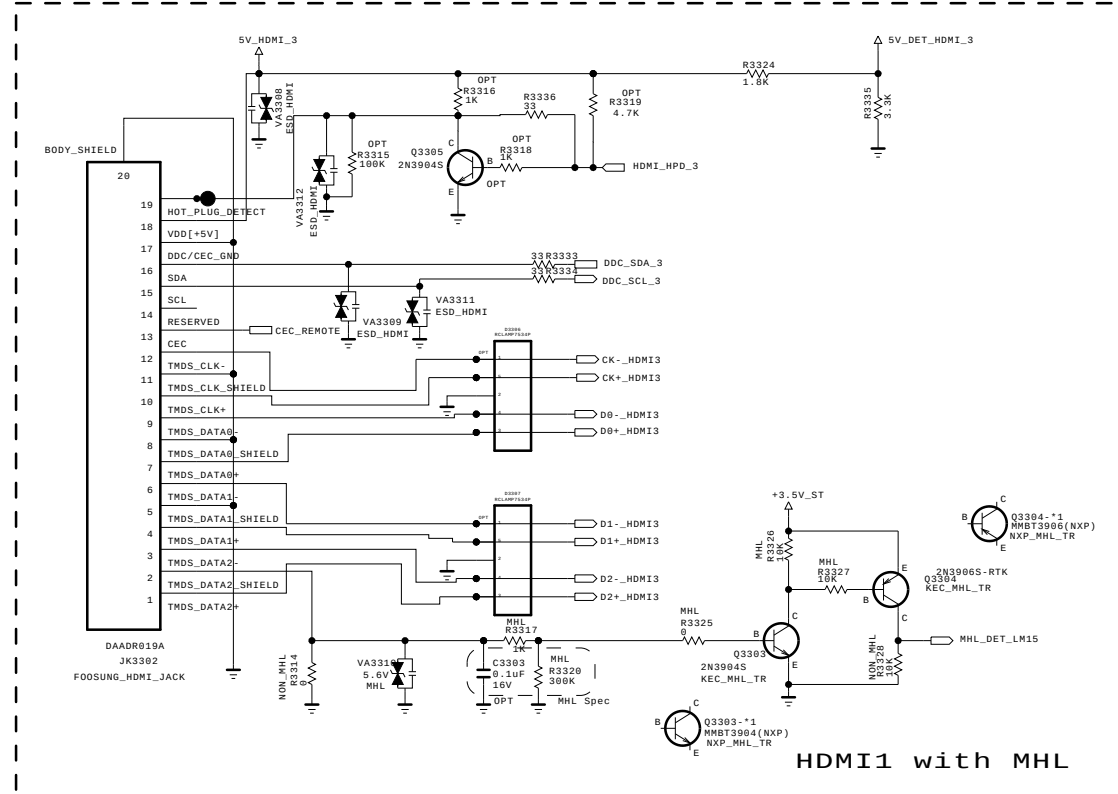
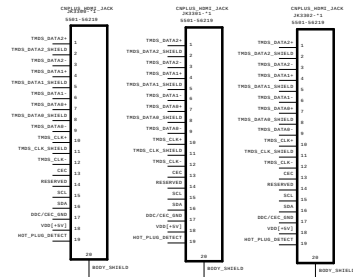




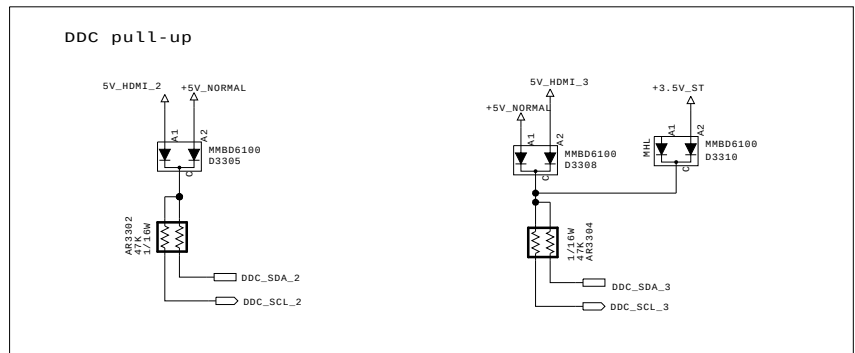
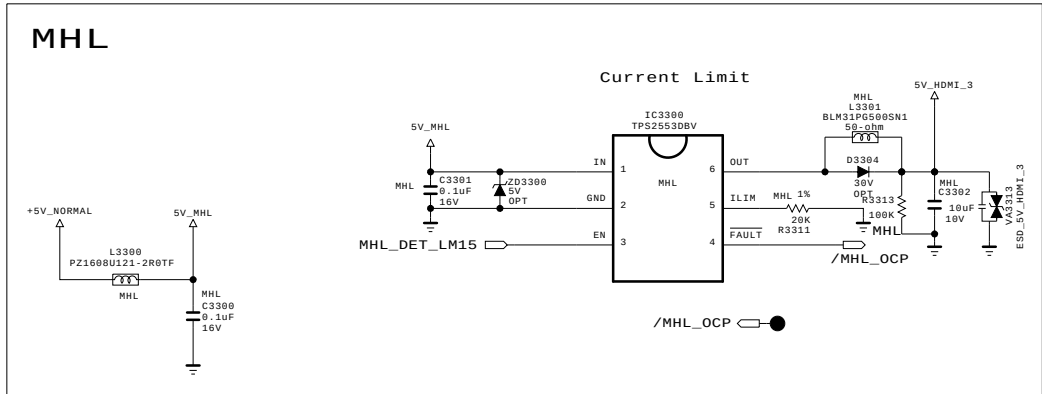
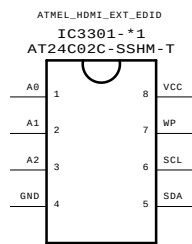
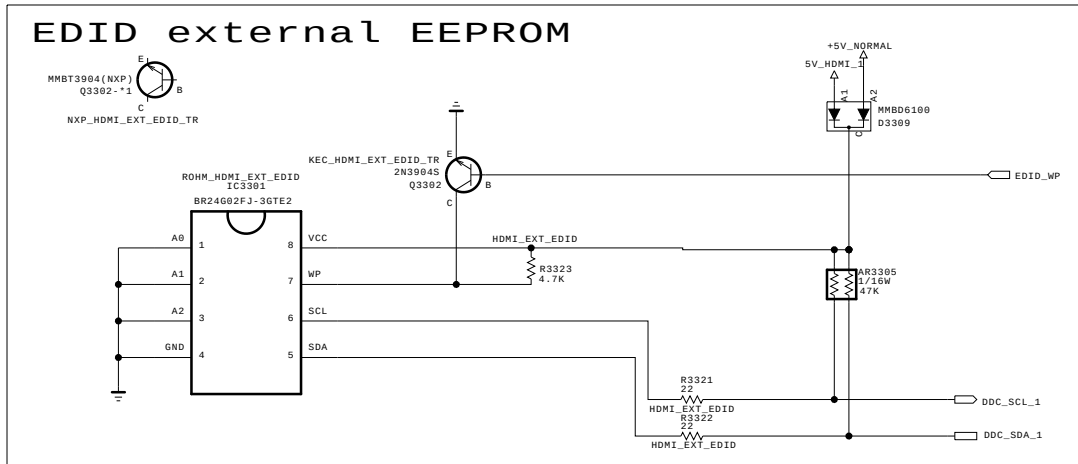
HDMI3



HDMI2 with ARC



HDMI1 with MHL

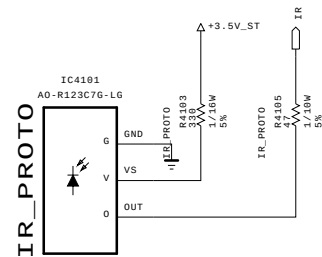
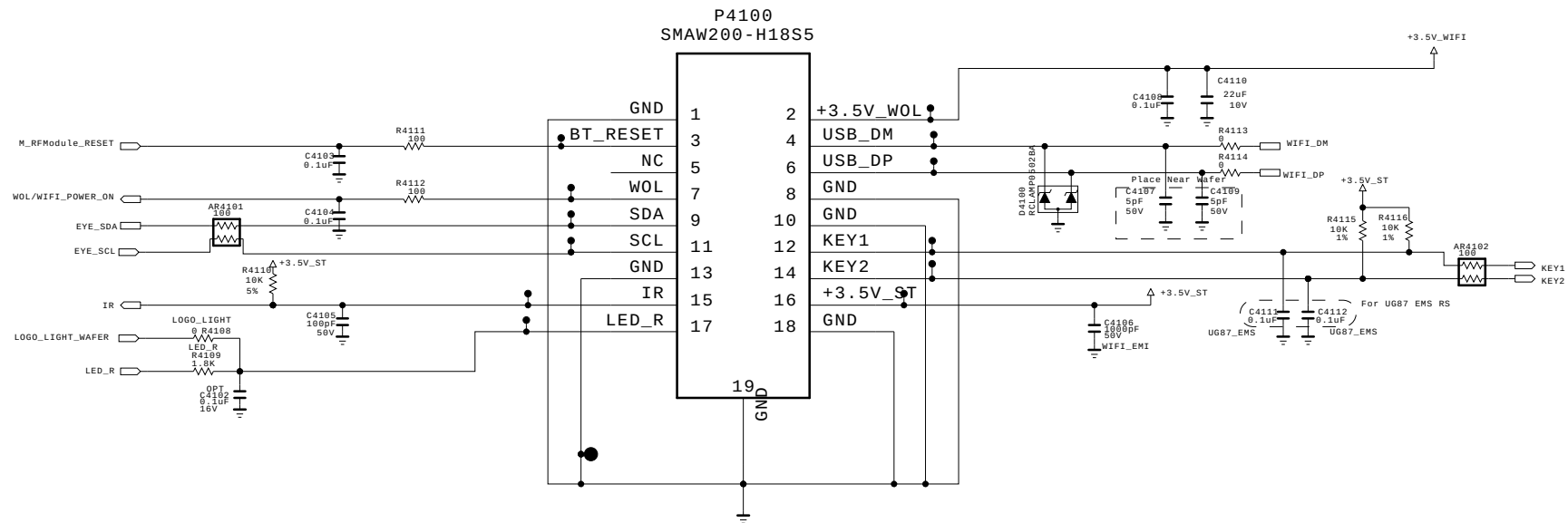


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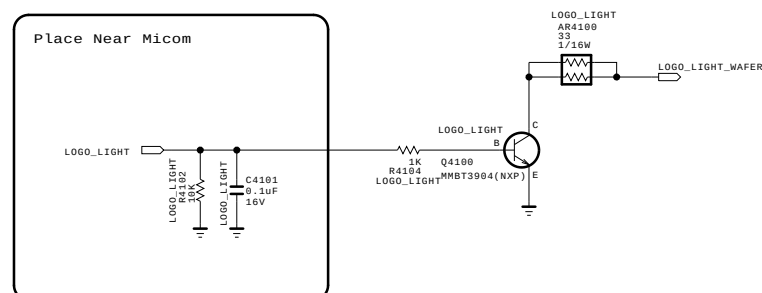
SECRET
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MODEL	LM15U	DATE	2014-11-04
BLOCK	HDMI	SHEET	10



SMD bottom -> Page 135



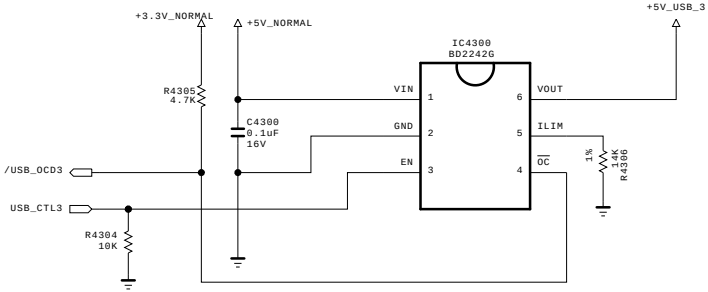
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LGElectronics



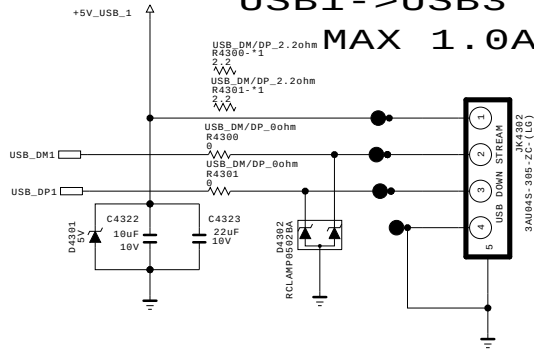
MODEL	LM15U	DATE	2014-12-25
BLOCK	IR/KEY	SHEET	41 /

+5V_USB FOR USB3->USB1

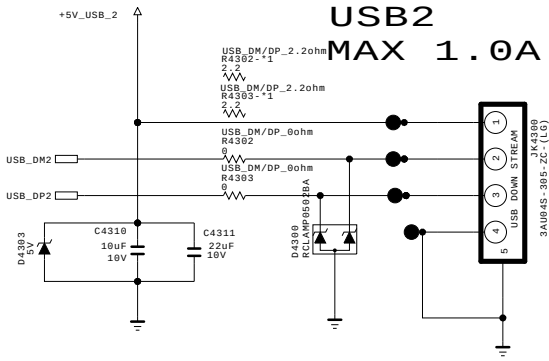
OCP USB3 ->USB1



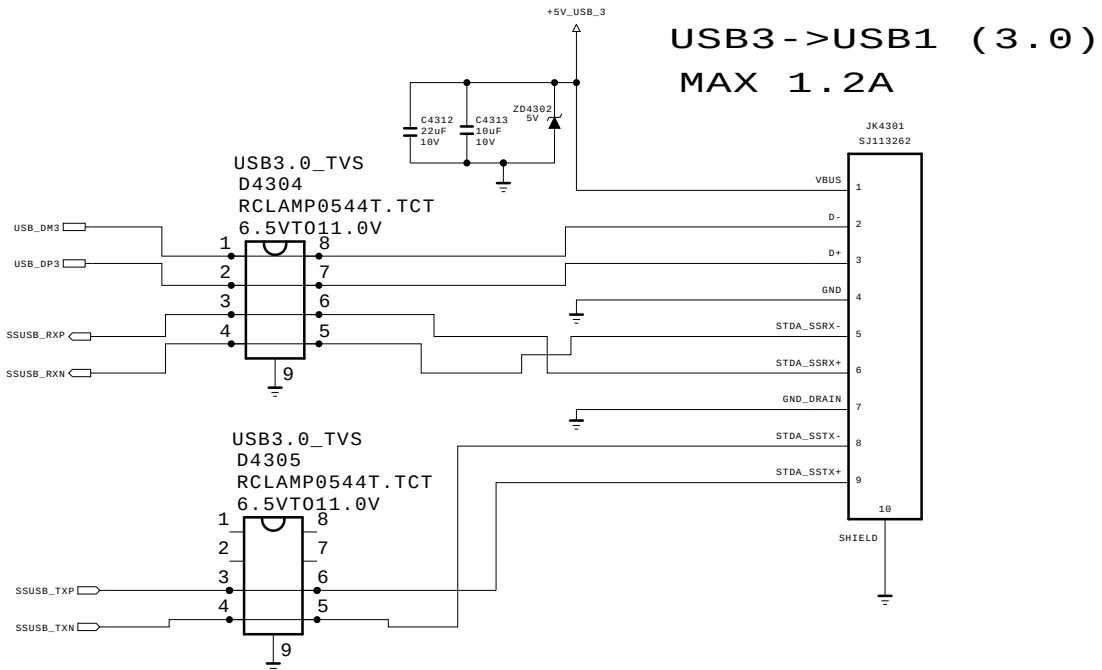
USB1 ->USB3
MAX 1.0A



USB2
MAX 1.0A



USB3->USB1 (3.0)
MAX 1.2A



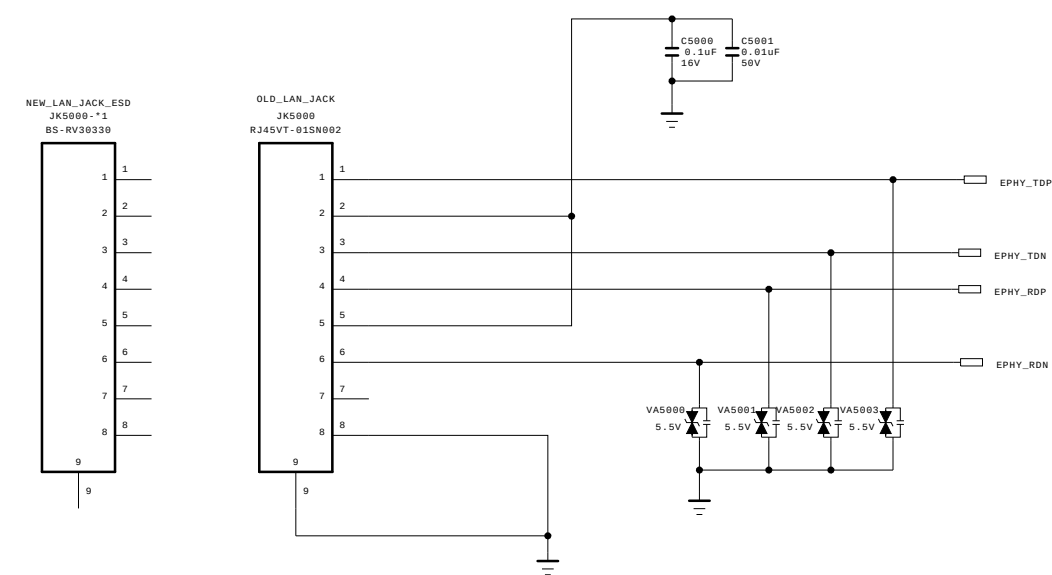
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MODEL	LM15U	DATE	2014-12-10
BLOCK	USB3_HUB	SHEET	43 /

Ethernet Block



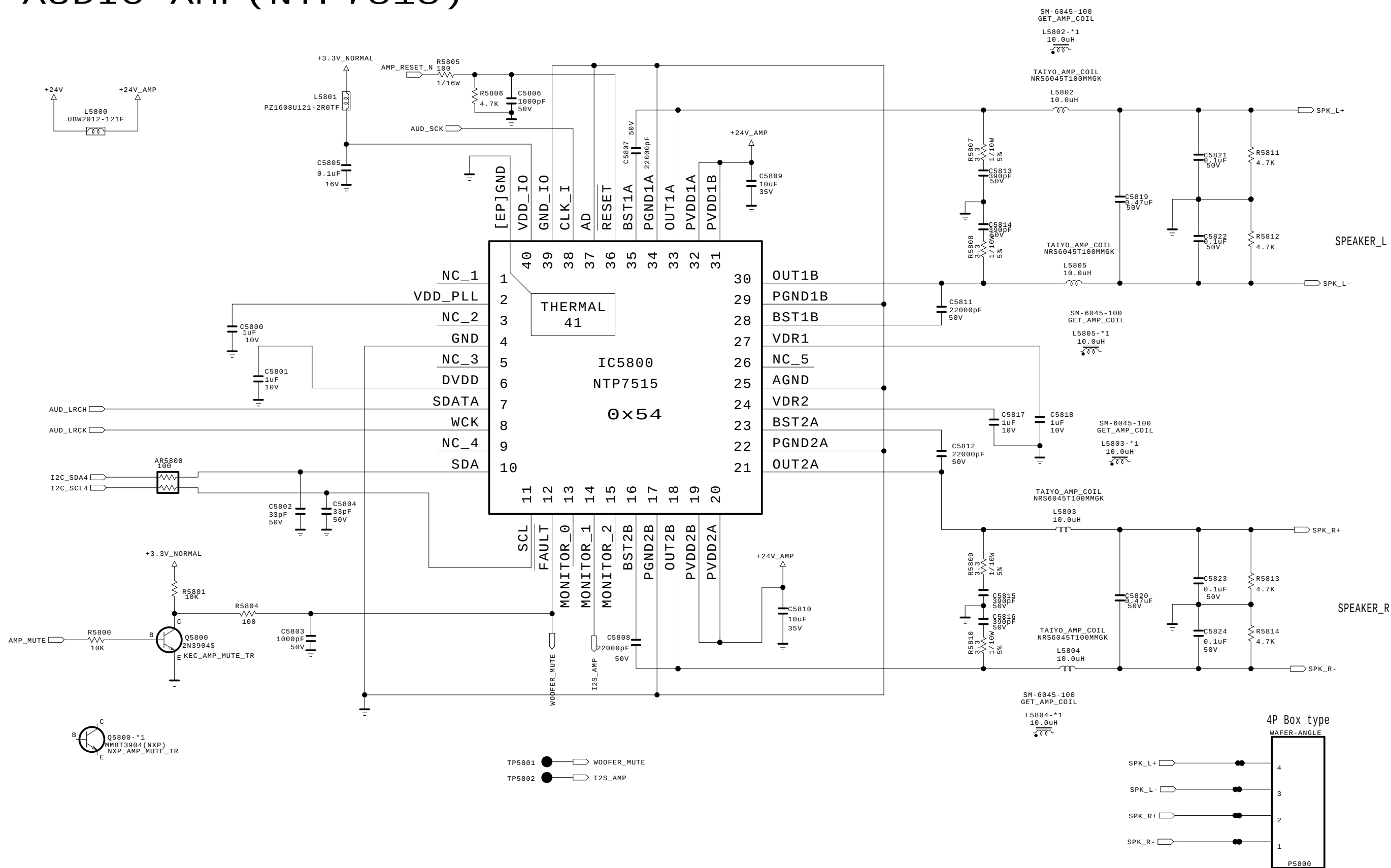
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MODEL	LM15U	DATE	2014-12-15
BLOCK	LAN_H	SHEET	50 /

AUDIO AMP(NTP7515)



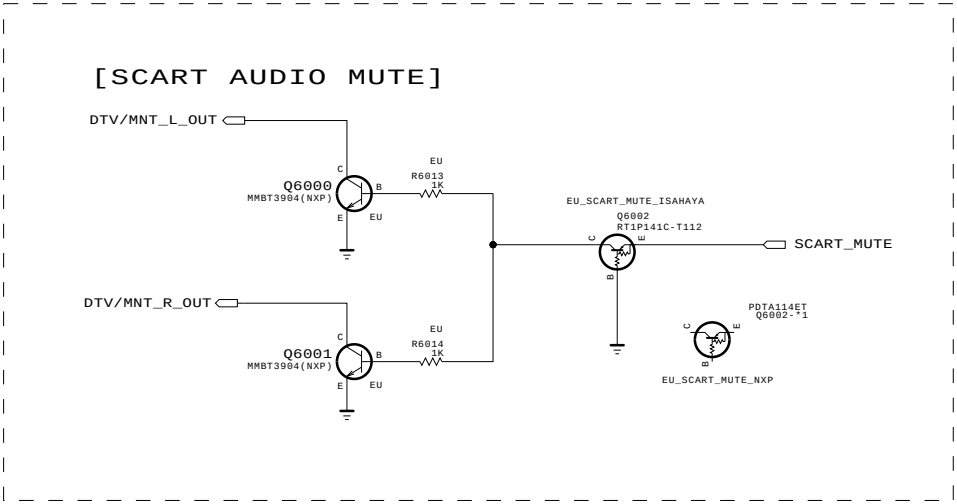
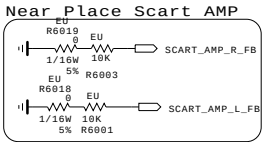
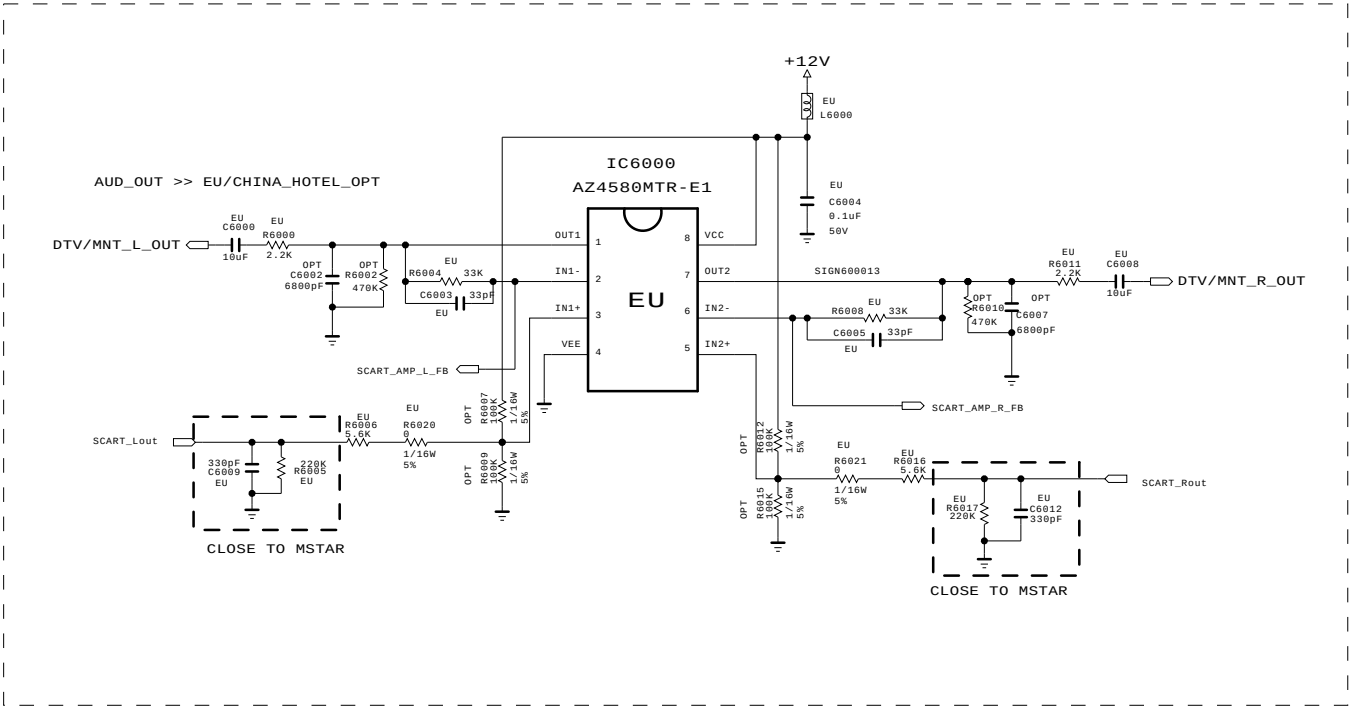
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MODEL	LM15U	DATE	2014-10-17
BLOCK	STM_AMP	SHEET	58 /



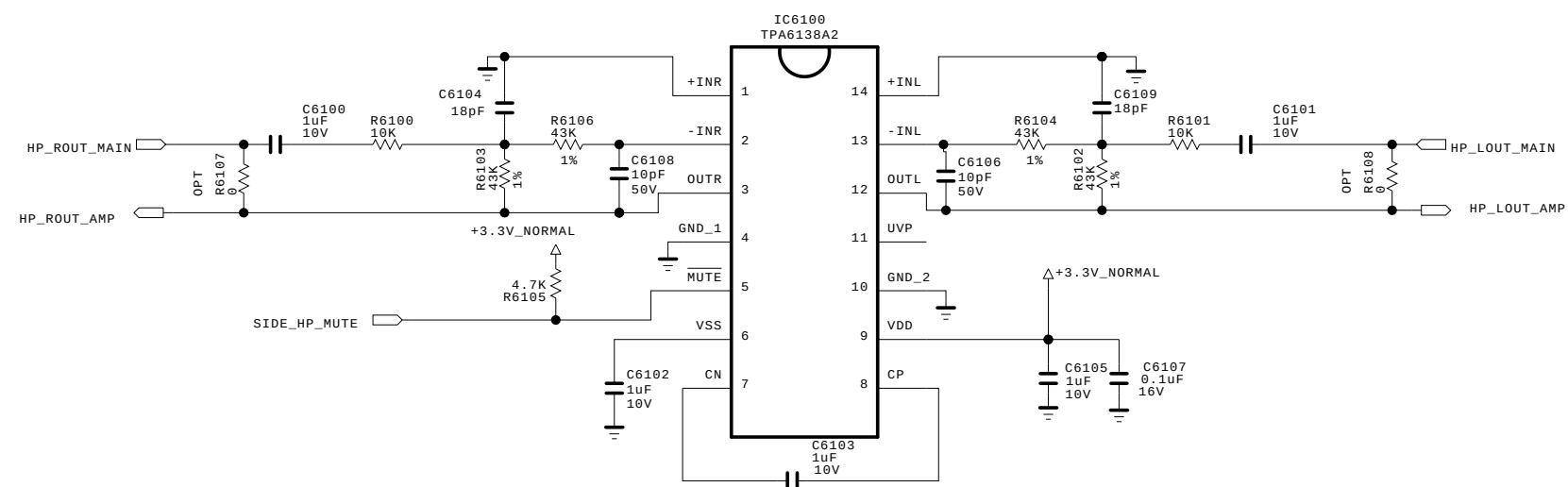
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MODEL	UF71/7500	DATE	2014-05-19
BLOCK	SCART AMP	SHEET	17

EARPHONE AMP



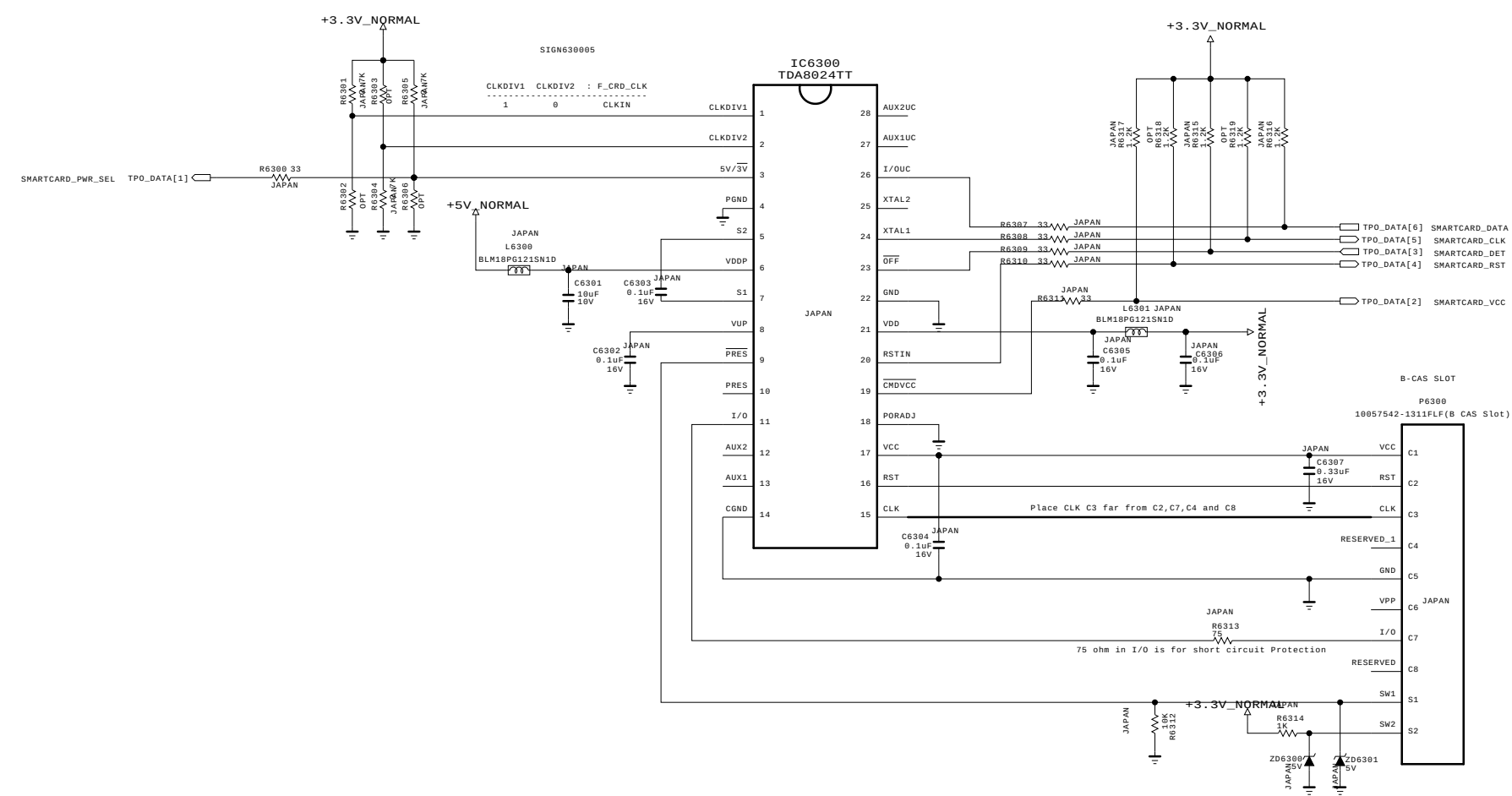
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MODEL	LM15U	DATE	2014-11-22
BLOCK	HP_AMP	SHEET	61 /

B-CAS (SMART CARD) INTERFACE



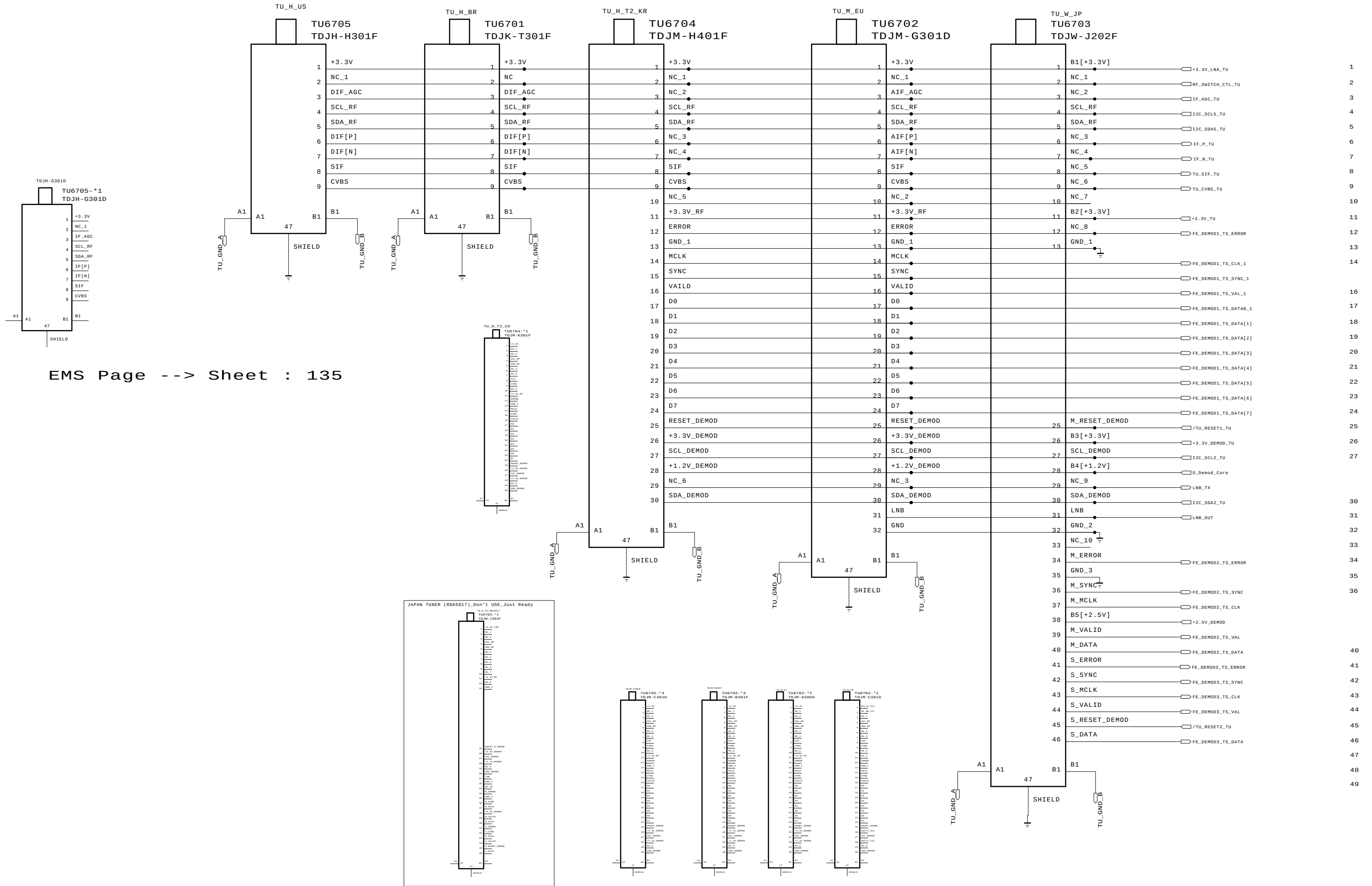
INT	CNDVCC	STATUS
HIGH	HIGH	CARD PRESENT
LOW	HIGH	CARD NOT PRESENT

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MODEL	JAPAN B-CAS	DATE	2011.04.17
BLOCK		SHEET	63 /



EMS Page --> Sheet : 135

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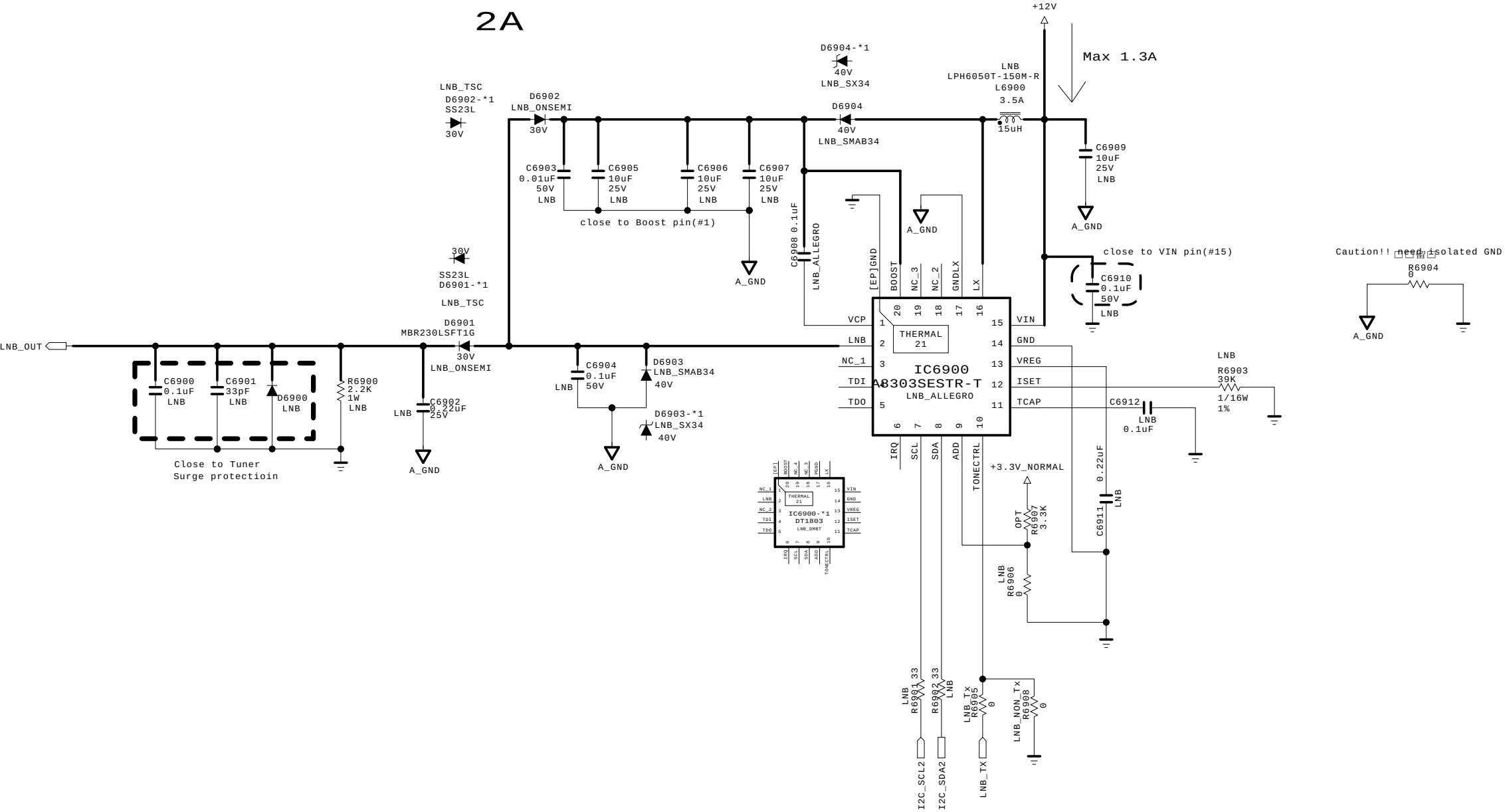
MODEL	LM15U	DATE	2014-09-11
BLOCK	TU_SYMBOL_H	SHEET	19

DVB-S2 LNB Part Allegro

(Option:LNB)

3A

Input trace widths should be sized to conduct at least 3A
Ouput trace widths should be sized to conduct at least 2A



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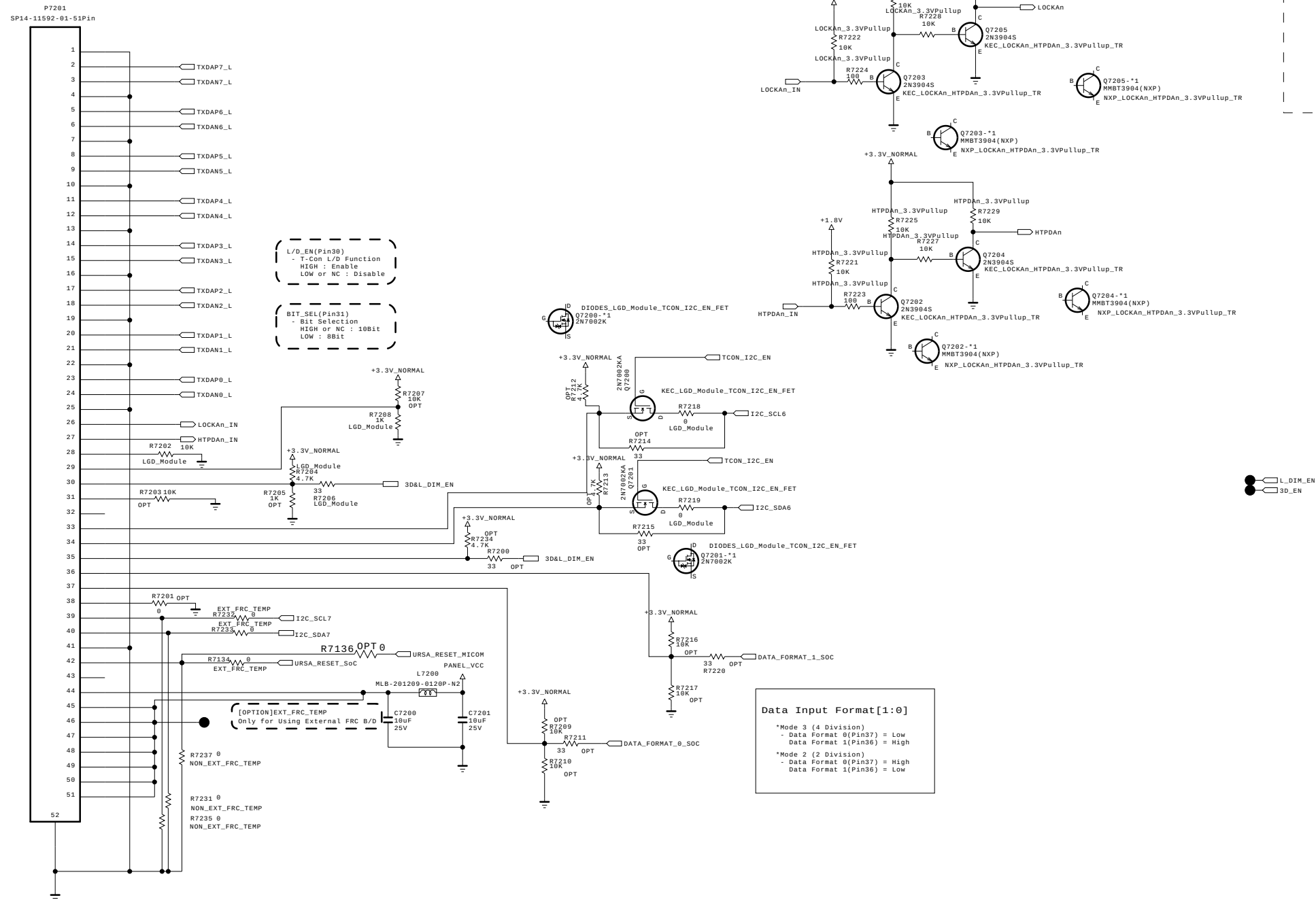
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LGElectronics

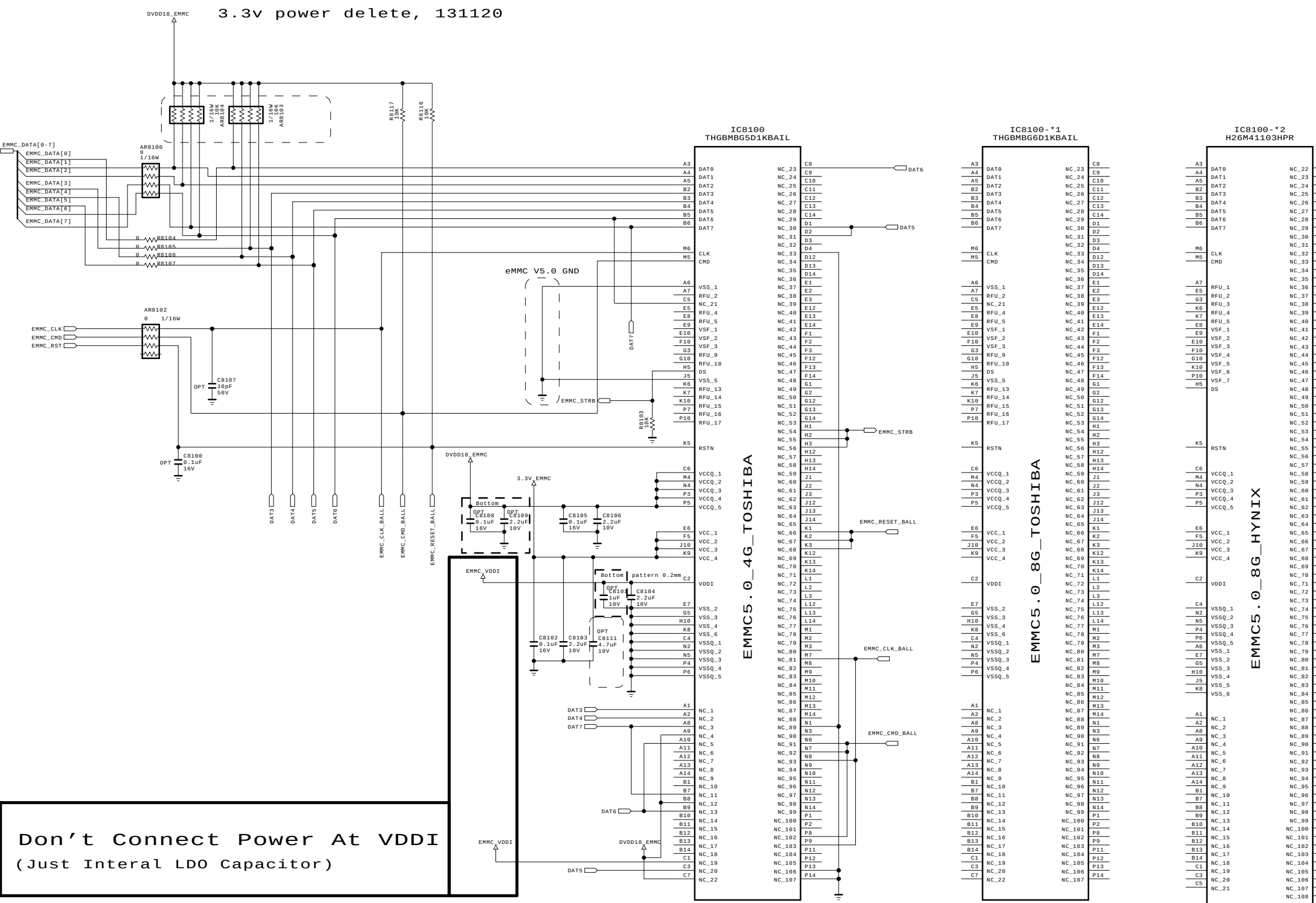
 LG ELECTRONICS

MODEL	LM15U	DATE	2014-08-25
BLOCK	LNB	SHEET	20 /

[51P Vx1 output wafer] Non REVERSE Type



eMMC I/F



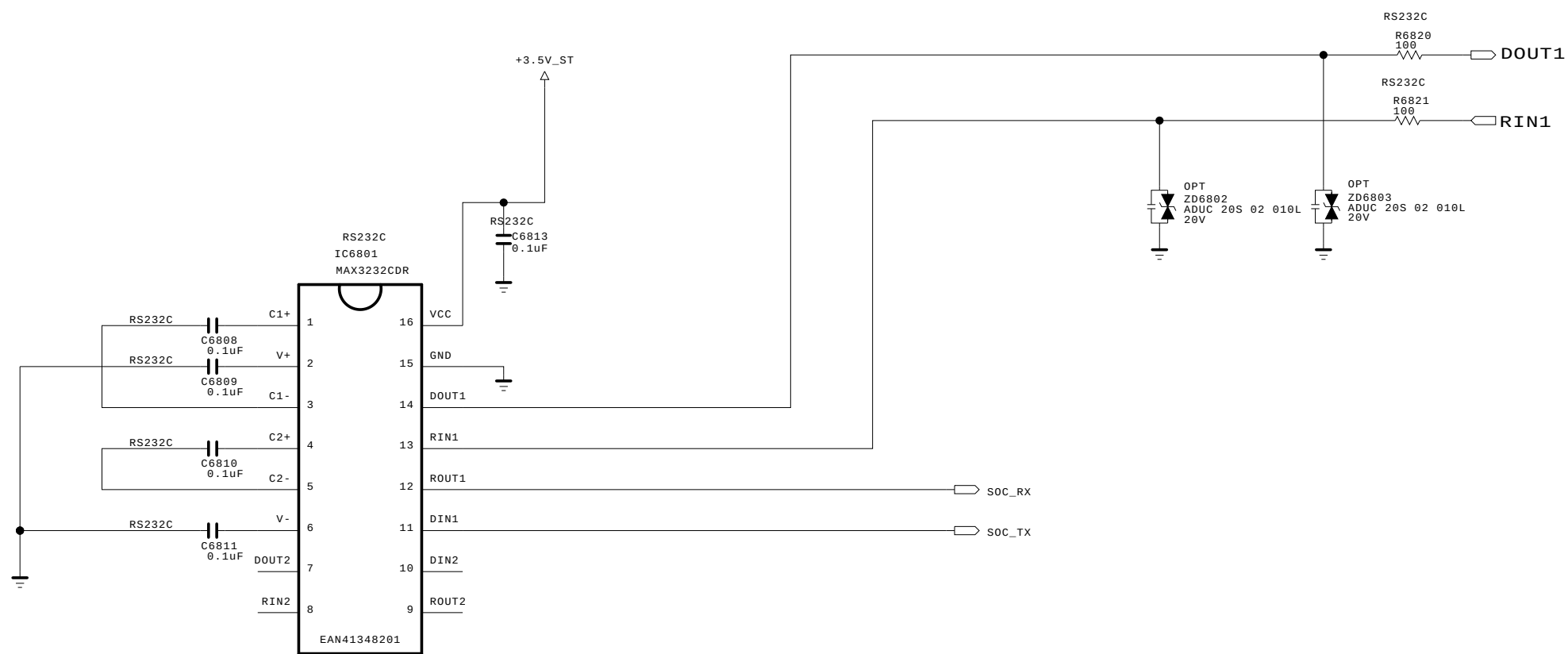
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MODEL	LM15U	DATE	2014-11-17
BLOCK	eMMC	SHEET	81 /

RS-232C Control INTERFACE



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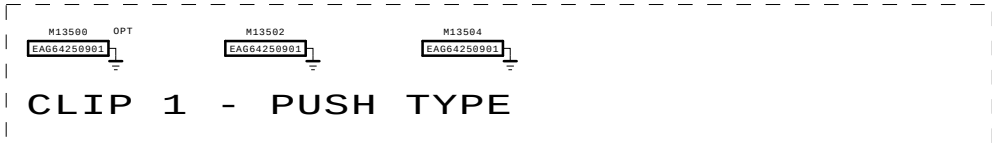
LGElectronics

 LG ELECTRONICS

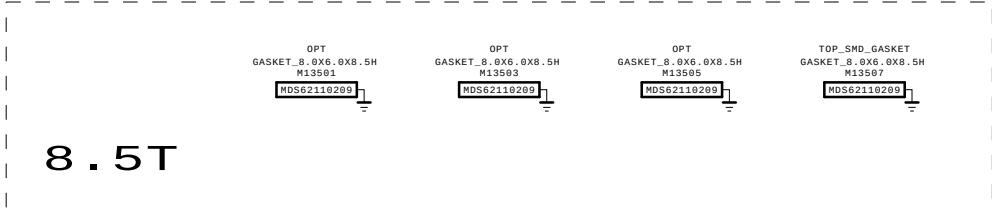
MODEL	UF71/7500	DATE	2014-05-19
BLOCK	RS232C	SHEET	22 /

[LM15U_ONLY]

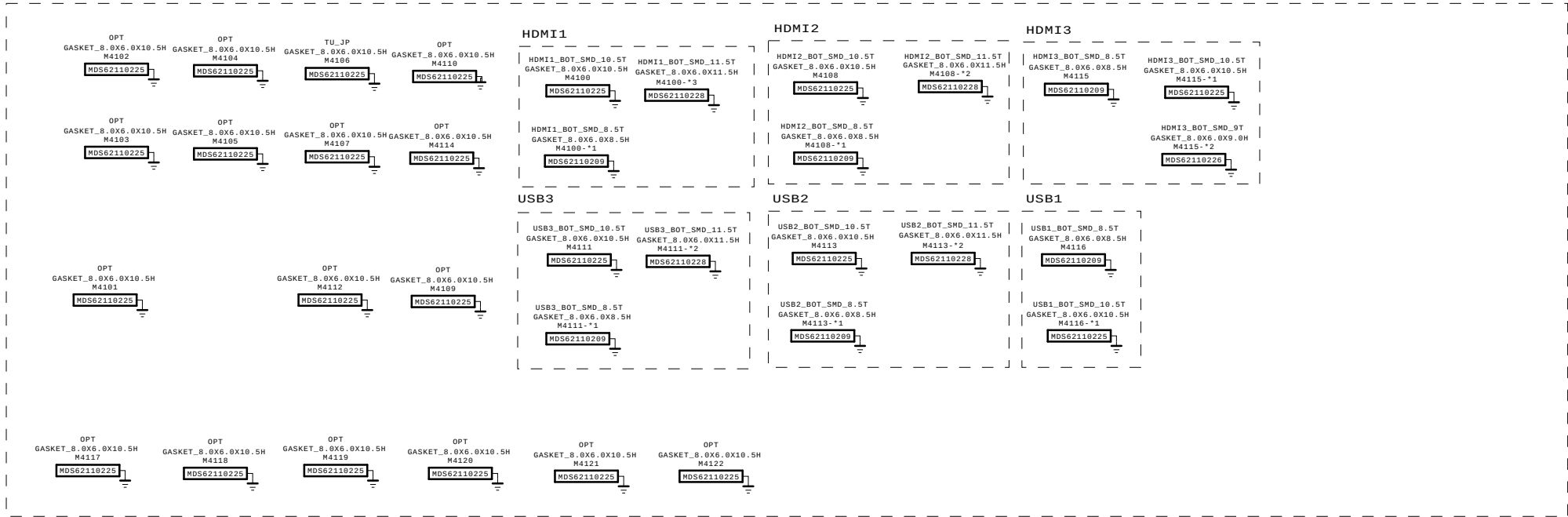
CLIP Top Side for Covershield To connect 28pin Wafer



SMD Top Side for Covershield



SMD bottom for ESD



THE  SYMBOL MARK OF THIS SCHEMATIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FIRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFACTURES SPECIFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE  SYMBOL MARK OF THE SCHEMATIC.

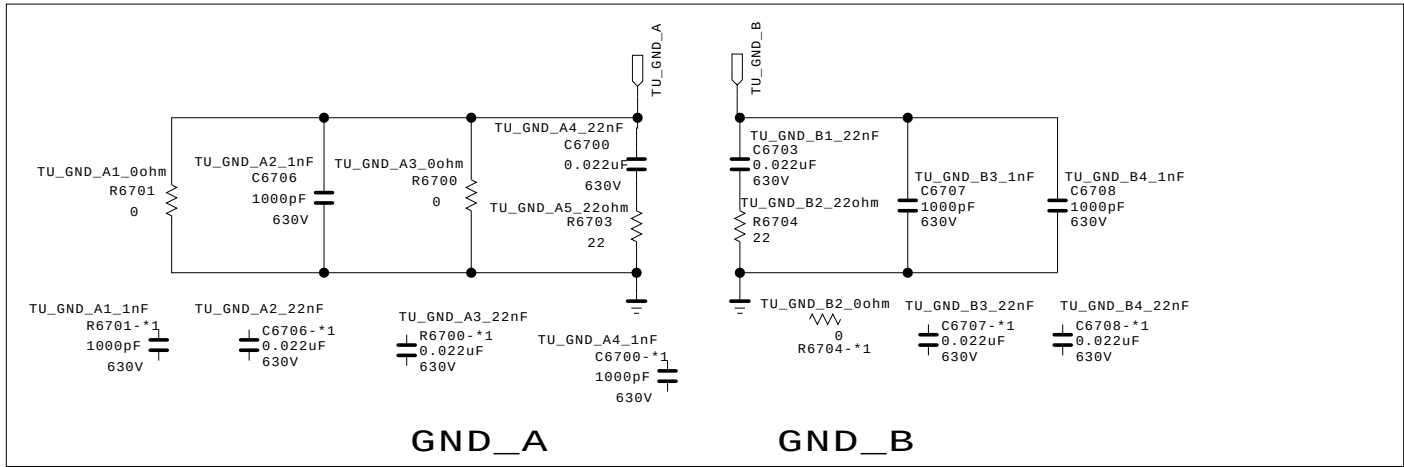
SECRET
LGElectronics



MODEL	LM15U	DATE	2014-11-27
BLOCK	LM15U_ONLY	SHEET	135 /

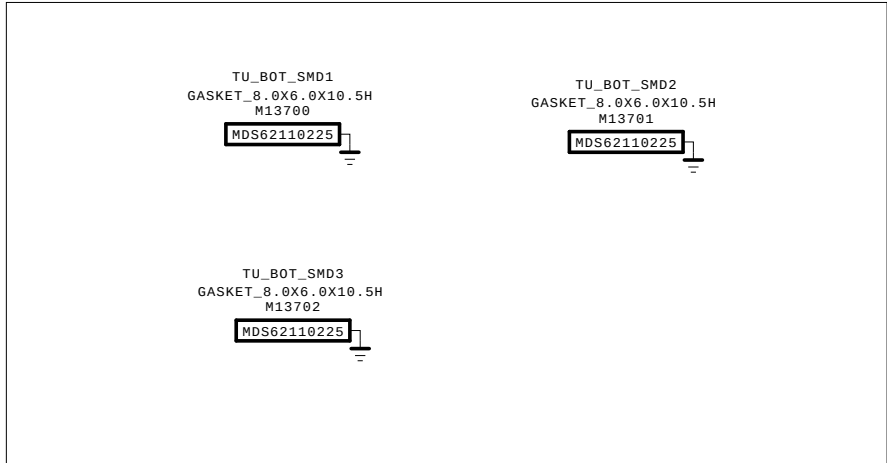
LM15U ONLY, LM15U+URSA

TUNER EMS GND SEPERATION



TU_GND_A	EU/CIS	AJJA	TW/COL	CN/HK	KR	North.AM	BR	JP
GND_A_1	0 ohm	0 ohm			X	0 ohm	X	0 ohm
GND_A_2	X	X			22 nF	X	22 nF	1 nF
GND_A_3	X	0 ohm			X	0 ohm	22 nF	0 ohm
GND_A_4	X	X			22 nF	X	22 nF	1 nF
GND_A_5	X	X			22 ohm	X	22 ohm	22 ohm

TU_GND_B	EU/CIS	AJJA	TW/COL	CN/HK	KR	North.AM	BR	JP
GND_B_1	X	X			22 nF	X	22 nF	X
GND_B_2	X	X			22 ohm	X	22 ohm	X
GND_B_3	1 nF	1 nF			22 nF	1 nF	22 nF	1 nF
GND_B_4	1 nF	1 nF			22 nF	1 nF	22 nF	1 nF



TU_BOT_SMD	EU/CIS	AJJA	TW/COL	CN/HK	KR	North.AM	BR	JP
SMD1								
SMD2								
SMD3								

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SECRET

LGElectronics

LG ELECTRONICS

MODEL	LM15U	DATE	2014-11-11
BLOCK	Tuner_GND	SHEET	137 /

